



Research article

Nanomaterial integration in micro LED technology: Enhancing efficiency and applications



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ABSTRACT

The micro-light emitting diode (μ LED) technology is poised to revolutionise display applications through the introduction of nanomaterials and Group III-nitride nanostructures. This review charts state-of-the-art in this important area of micro-LEDs by highlighting their key roles, progress and concerns. The review encompasses details from various types of nanomaterials to the complexity of gallium nitride (GaN) and III nitride nanostructures. The necessity to integrate nanomaterials with III-nitride structures to create effective displays that could disrupt industries was emphasised in this review. Commercialisation challenges and the economic enhancement of micro-LED integration into display applications using monolithic integrated devices have also been discussed. Furthermore, different approaches in micro-LED development are discussed from top-down and bottom-up approaches. The last part of the review focuses on nanomaterials employed in the production of micro-LED displays. It also highlights the combination of III-V LEDs with silicon LCDs and perovskite-based micro-LED displays. There is evidence that efficiency and performance have improved significantly since the inception of the use of nanomaterials in manufacturing these.

1. Introduction

In advancing the technology of micro-LED, particularly for augmented and virtual reality (AR and VR), increasing requests for high-resolution displays have played a key role [1,2]. The backdrop of LED can be traced back to 1927 through Oleg Losev's pioneering work on the first LED, which was originally limited to infrared, red and yellow colors [3]. Shuji Nakamura's demonstration of an efficient blue LED in 1994 was a turning point in history. Materials such as Gallium Nitride (GaN) or Indium Gallium Nitride (InGaN) were used in these LEDs to emit blue light with high efficiency [3]. The relationship between Gallium Nitride (GaN) and Indium Gallium Nitride (InGaN) lies in their role as key materials in Light-Emitting Diodes (LEDs), specifically for efficient blue light emission. GaN is commonly used for blue and green LEDs, while

InGaN, as a ternary semiconductor alloy, is utilised to fine-tune the emission wavelength and produce various colors, including blue [4]. On the other hand, Nuclear Magnetic Resonance (NMR) spectroscopy is a technique employed to study molecular properties. It plays a vital role in analysing compounds related to LED materials or investigating the chemical properties of substances used in LED production [5,6]. Semiconducting materials combined with phosphors converts blue light into red and green, resulting in a wide spectrum of white light in the field of white LED lighting. These LEDs were noted for their energy efficiency, converting a significant proportion of electric energy into visible blue light that reduces heat waste and ensures a long lifetime. Blue LEDs are crucial in technologies such as Blu-ray Disc players and high-density optical storage systems [4]. Micro-LEDs consist of microscopic LED chip arrays that improve energy efficiency, brightness, response times,

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and lifespan [7]. The semiconductor chip is made of p-type and n-type layers, causing the existence of a p-n junction that leads to light emission. The presence of holes in the p-type layer and electrons in the n-type constitute their positive and negative charge carriers, respectively. The establishment of the p-n junction entails a depletion region as a result of charge carrier migration. When applying forward bias, it implies attaching a positive voltage at the p-side and a negative voltage at the n-side, thus reducing the junction's potential barrier. Consequently, this bias allows for the movement of charge carriers (electrons and holes) across the junction that leads to light emission. The energy bandgap of the semiconductor material determines the color of emitted lights [8,9]. Phosphor-coated white LEDs are essential for display technology, which uses blue light to create dynamic colours. Display systems based on red, green, and blue light-emitting diodes (RGB-LEDs) use additive colour mixing to produce vivid images. It has three separate LED chips namely, red, green and blue. By manipulating the intensity of these three colours, many additional colours can be obtained. The applications of RGB LEDs are prevalent in changing or customisable lighting situations [10,11]. Pixel pitch, display resolution determination and Nits are some of the key factors that influence the efficiency of LED displays. Nits in LED displays, are a unit of measurement for luminance, representing the amount of visible light emitted by a source per unit of area. Essentially, Nits indicate the brightness of an LED display, and the term is derived from the Latin word "nitere," meaning to shine or glitter. To expand the lifespan of LED displays, it is necessary to comply with Electromagnetic Compatibility (EMC) standards [12–14]. At present, micro-LED displays have outperformed OLEDs or Organic Light Emitting Diodes in terms of brightness and lifetime. Apple's new approach involves replacing Thin-Film Transistor (TFT) backplanes with micro-Integrated Circuit (IC) drivers for better self-reliance [15,16]. The recent improvements in micro-LED technology, as a case in point, suggest that micro-LEDs could be a practical replacement for Organic Light-Emitting Diodes (OLEDs) and Liquid Crystal Displays (LCDs) in some cases [17]. OLEDs are made from carbon-based organic compounds and consist of multiple organic layers sandwiched between a transparent anode and a reflective or transparent cathode. The ability to emit light directly upwards makes OLEDs unique [18]. Jin et al. [7] improved quantum efficiency in microdisk LEDs, whereas Tian et al. [19] invention of flexible micro-LED arrays enabled fast data transmission. Fig. 1 illustrates improved pixel density for micro-LEDs from 2007 to 2019, portraying the advancements amid the challenges faced by full-color large-scale displays, reaching approximately 2000 pixels per inch (PPI) [20]. Singh et al. [21] highlighted the rapid modulation of micro-LEDs for visible light communication at high speed, thus demonstrating their appropriateness in quick data transfers. Also, Choi et al., [22] integrated GaN micro-LED arrays with sapphire microlenses and emphasises the critical role of optical engineering. Tull et al., [23] integrated LEDs with nanomaterials containing Group III-V elements from the periodic table and silicon thin film transistors to enhance the efficiency. The combination of Group III and V elements leads to compound semiconductors that are important for

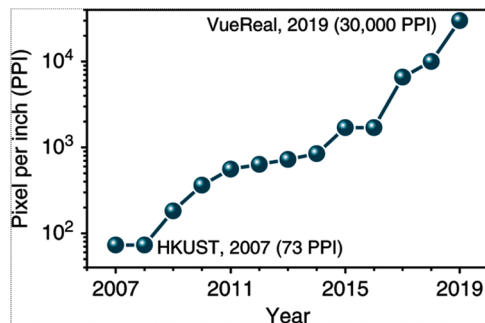


Fig. 1. Pixels per inch (PPI) roadmap of μ -LED displays from 2007 to 2019 [20].

electronics and optoelectronics. For continuous research and development, refining fabrication techniques and overcoming technical challenges are necessary to bridge the gap between current achievements and the continuous challenge of achieving high PPI displays. μ LED technology is transforming the landscape of Augmented Reality (AR) experiences [24,25]. Saphlux's micro-LED microdisplay employed Nano-Patterned Gallium Nitride (NP GaN), as well as quantum dots, with a superlative light conversion efficiency in 2020 of over 90% [26]. μ LED technology made significant progress in 2022 and 2023. Play-Nitride developed a prototype with a density of up to 4536 pixels per inch (PPI) [27]. Jade Bird Display introduced a native color monolithic micro-LED microdisplay using Indium Gallium Nitride (InGaN) and Aluminum Indium Gallium Phosphide (AlInGaP) epitaxy. MIT's vertically stacked μ LEDs achieved an impressive pixel density of 5100 PPI via a two-dimensional material-based layer transfer process [28]. In its advances in the area of full-color micro-LEDs, PlayNitride has creatively integrated red, green, and blue (RGB) micro-LEDs onto one chip. At the pixel level, advanced technologies are employed to make each pixel emit red, green, and blue lights independently for full-color capabilities [27].

Remarkably, the landscape of display technology has undergone a great revolution due to commendable changes in micro-LED technology, seen in its brightness, energy consumption and excellent picture quality. The struggle to produce high Pixel Per Inch (PPI) displays, especially in Augmented Reality is ever-recurring. Micro-LED's developmental trajectory is detailed in Table 1, focusing on its evolution and current state. Inarguably, micro-LEDs are revolutionary components in display technology. Nevertheless, manufacturing a micro-LED is complicated,

Table 1

Chronological progress and milestones in micro-LED Array development from 2000 to 2023.

Year	Micro-LED Description	Key Findings
2000	Theoretical groundwork for micro-LED by Jiang et al. [7]	Laid theoretical and experimental foundations for micro-LED technology
2001	10 × 10 blue micro-LED array with a 12 μ m chip size by Jiang et al. [29]	Fabricated a small-scale blue micro-LED array
2004	64 × 64 UV micro-LED array with a 20 μ m chip size by Dawson et al. [22]	Successfully developed a large-scale UV micro-LED array
2007	16 × 16 blue micro-LED array with a 72 μ m chip size by Dawson et al. [30]	Demonstrated a small-scale blue micro-LED array using a flip-chip structure
2008	64 × 64 blue, green, and UV micro-LED array with a 20 μ m chip size by Poher et al. [31]	Created a multi-color micro-LED array with a small chip size
2011	160 × 120 green micro-LED array with a 12 μ m chip size by Lin et al. [32]	Established a high-resolution green micro-LED array using indium bump flip-chip bonding
2013	UV and RGB micro-LED arrays with a 50 μ m chip size (360 PPI) by Liu et al. [33]	Achieved UV and RGB micro-LED arrays with moderate resolution
2014	Blue micro-LED arrays with a 15 μ m chip size (1700 PPI) by Liu et al. [34]	Developed high-resolution blue micro-LED arrays
2015	128 × 128 RGB micro-LED array with a 35 μ m chip size on a UV-based array by Kuo et al. [35]	Fabricated an RGB micro-LED array using UV and quantum dots (QDs) spraying technology
2017	Improved QD spraying accuracy and reduced pixel crosstalk using a photoresist mold by Kuo et al. [36]	Enhanced accuracy in QD application to micro-LED arrays
2020	Green micro-LED array with a 3.6 μ m chip size on a sapphire substrate by Wang et al. [37]	Developed a green micro-LED array without involving the mesa etching process
2023	Vertical full-color micro-LED array with 5100 PPI and a 4 μ m chip size by researchers from MIT, US, and South Korean Universities [28]	Achieved the highest PPI with the smallest chip size using 2D material layer transfer technology

making the cost of production higher than anticipated, and there are difficult challenges involved as well. Samsung has come up with new-generation TV sets based on this trend. Apple, for instance, reveals this potential through disruptive innovations, and it is believed that the introduction of micro-LED will pave the way for a new era of displays. These strategies often bring to the market new technologies, business models, or products that disrupt existing practices and can result in significant market shifts. This review provides a detailed analysis of the use of nanomaterials in micro-LED technology with an emphasis on increased efficiency and diverse applications. It is important to examine specific nanomaterials and their utilisation in micro-LED more closely to gain a full understanding of their roles. The importance of such materials goes beyond just improved efficiency; it also covers enabling various functions in the micro-LED sphere. The use of nanomaterials allows for improvements in performance and functionality. Therefore, the paper highlights the versatility of nanomaterials such as quantum dots and nanostructured phosphors in different kinds of micro-LED applications where further studies are essential for better insights.

2. Nanomaterials and III-nitride nanostructures

III-Nitride nanostructures consisting of nitrides of gallium (GaN), aluminum (AlN) and indium (InN), are key building blocks for the intricate world of semiconductor physics and technology [38]. Proximity to the band edge makes them ideal for use in micro-LEDs, increasing their brightness and energy efficiency. The emission wavelength of micro-LEDs can be precisely controlled by the compositional

flexibility provided by III-Nitride alloys [38]. Classic considerations usually recommend Aluminum Indium Gallium Nitride (AlInGaN) and Aluminum Indium Gallium Phosphide (AlInGaP) alloys for this intricate tuning process [39–41]. To attain high-speed operation in micro-LEDs, particularly needed in high image resolution displays and fast-refresh-rate screens, this III-Nitride material has superior electron mobility [40,41]. The toughness and steady performance of micro-LED displays become possible due to the sturdy quality of III-Nitride materials, particularly gallium nitride (GaN) [19]. The use of nanoscale engineering further improves flexibility in design [42]. GaN and its alloys, such as AlGaN (Aluminum Gallium Nitride) and InGaN (Indium Gallium Nitride), with an emphasis on GaN that directs and governs the micro-LED fabrication process, significantly enhance light emission efficiency [43]. Careful control of the emission wavelength via deliberate modulation of III-Nitride alloy composition is essential for this purpose, in which Gallium Nitride (GaN) takes a lead role [44,45]. III-Nitride nanostructures' inherent features make them key components in intricate micro-LED designs. Direct bandgap, tunable wavelength, high electron mobility, durability and miniaturisation capabilities set new limits for achievable advancements with this technology [37]. The basis of comprehending cross-substrate applications and offering solutions lies in understanding GaN nanowire growth mechanisms. The first few steps of GaN nanowire growth were carried out using Plasma-assisted Molecular Beam Epitaxy (MBE) to show that it was possible to grow on various surfaces [46]. With successful selective area growth (SAG) processing for GaN nanowires on different substrates, attention has shifted to versatility and wide applicability. By using a titanium nitride

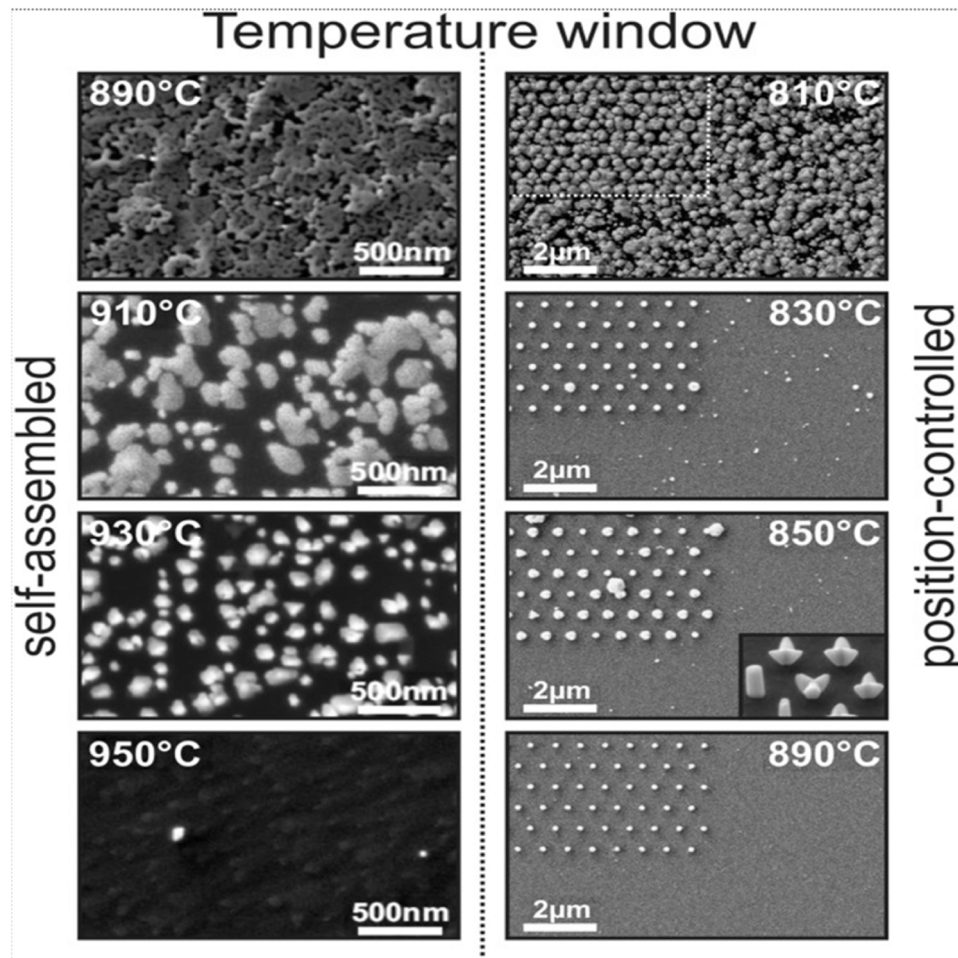


Fig. 2. SEM images, illustrating (left) self-assembled and (right) selective area growth (SAG) of GaN nanowires (NWs) on diamond at various substrate temperatures [48].

nanomask, this not only demonstrates that the method can be applied to various materials but also expands the scope for future advances in nanowire-based devices [47]. A careful study into temperature regions needed for growing GaN nanowires over diamond substrates is given. Self-assembled growth requires a higher absolute substrate temperature within a narrow range of approximately 20 K, whereas SAG occurs in a larger window of about 60 K. SEM images in Fig. 2 compellingly depict a seamless transition from a layer-like morphology to well-defined nanowires at elevated temperatures. These serve as persuasive evidence of the intricate processes taking place. These insights into nucleation site distribution and nanowire morphology offer relevant knowledge for growth condition optimisation. SEM images depict (left) self-assembled and (right) SAG of GaN nanowires (NWs) on diamond at different substrate temperatures. With approximately 0.5% and 1.0% for self-assembled and SAG, respectively, effective III/V flux ratios have been used in both methods [48]. This is because the ratio of group III to group V elements in the precursor chemicals (III/V flux ratio) was estimated to be about 0.5% for self-assembled growth and 1.0% for SAG growth. This parameter determines the composition and properties of semiconductor materials during their development, making the III/V flux ratio vital in their growth. In this case, it gives an idea of how each element is used during the growing processes [49,50].

Fig. 3 provides information on the study of GaN nanowire growth on diamond substrates using various microscopy techniques. Fig. 3(a) displays a cross-sectional High-Angle Annular Dark-Field Scanning

Transmission Electron Microscopy (HAADF STEM) and color-coded Energy-Dispersive Electron Spectroscopy (EELS) mapping, revealing the structure of a self-assembled GaN nanowire on a diamond substrate rather than presenting atomic species. Aberration-corrected Annular Bright-Field Scanning Transmission Electron Microscopy (ABF STEM), rendered by false colors, is shown in Fig. 3(b), indicating wurtzite Ga-N bilayers and revealing the N-polarity of the nanowire. In Fig. 3(c), a High-Resolution Transmission Electron Microscopy (HRTEM) image was taken to demonstrate GaN interacting with diamond, facilitating a more profound understanding at the level of structure. Finally, power spectra are provided in Fig. 3(d), displaying that an epitaxial relationship between GaN nanowire and diamond substrate exists [51].

Exploration of the growth of InGaN nanowires challenge existing assumptions and marks a radical break with previous understanding. To achieve quasi-stoichiometric growth by aligning the metal flux with nitrogen flux, the goal reverses the course for all growth parameters. The motivation for this pursuit is to have total mastery over features of InGaN nanowires, which are crucial elements in optoelectronic devices like LEDs and photodetectors [52]. Gallium Nitride (GaN) is another semiconductor with a wide bandgap and is used in many electronic and optoelectronic devices [53]. The InGaN nanowire based on GaN nanowires under different growth conditions is depicted in Fig. 4, contributing to a broader understanding of the InGaN nanowire growth kinetics. The study of InGaN nanowires emitting at around 600 nm unveils the effect of growth conditions, as reflected by various optical

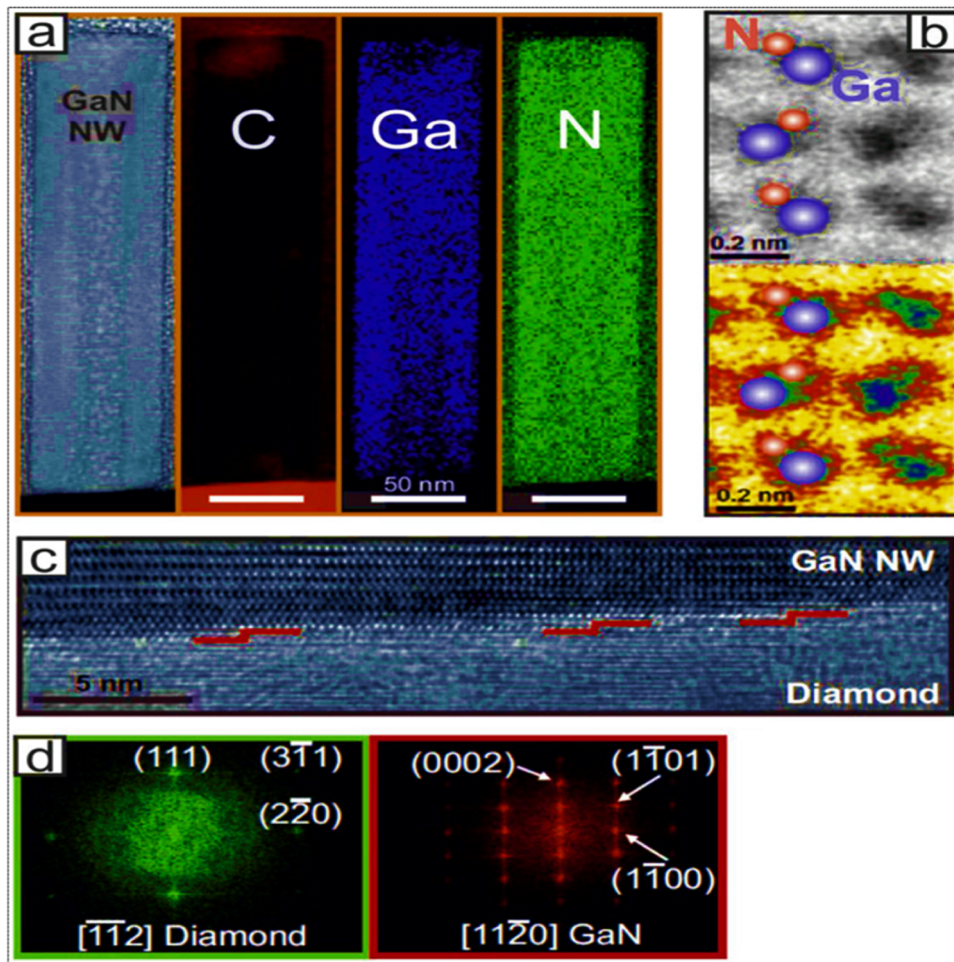


Fig. 3. (a) Cross-sectional HAADF STEM and color-coded EELS mapping reveal different atomic species in a self-assembled GaN nanowire (NW) on a diamond substrate, (b) An Annular Bright-Field Scanning Transmission Electron Microscopy (ABF STEM) image, represented in false colors (e.g., used to enhance contrast and highlight specific details that may not be easily distinguishable in grayscale), exhibits wurtzite Gallium nitride (Ga-N) bilayers, indicating N-polarity, (c) High-resolution transmission electron microscopy (HRTEM) image at GaN/diamond interface, (d) Epitaxial relationship by power spectra [51].

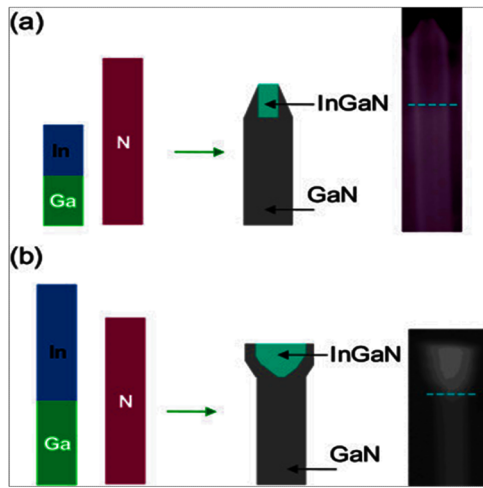


Fig. 4. Schematics of Indium Gallium Nitride (InGaN) Nanowire (NW) growth on Gallium Nitride (GaN) NW base. (a) Growth in nitrogen-rich conditions illustrating the formation of a self-controlled InGaN/GaN core/shell structure, (b) Growth of the InGaN section in metal-rich conditions, resulting in the widening of the InGaN section and the formation of a core/shell structure [53].

behaviors and structural changes [53]. The integration of III-Nitride nanostructures into micro-LEDs is the leading cause of technological advancement in the field of advanced electronics and optoelectronics. This effort, in conjunction with knowledge about growth mechanisms as well as substrate-dependent behaviors, establishes a robust foundation for nanowire-based devices and cutting-edge display systems [52]. In the exploration of Gallium Nitride (GaN) nanowire growth, gaining insight concerning growth dynamics and interface characteristics is crucial for optimising conditions. The growth process schematically depicted in Fig. 4(a) demonstrates self-controlled InGaN/GaN core/shell structure formation under nitrogen-rich conditions. Consequently, through growth in metal-rich conditions, the section of InGaN widens, expanding the InGaN section to form a core/shell structure as illustrated in Fig. 4(b). However, it emphasises that, contrary to common belief, closely matching metal flux with nitrogen flux is crucial for quasi-stoichiometric growth. Fig. 4 demonstrates that different InGaN structures can be obtained by varying conditions during the process. Therefore, it provides an improved understanding necessary to optimise growth conditions to obtain desired results in InGaN nanowire formation, where these outcomes are needed. This understanding is essential for optimising growth conditions and achieving desired outcomes in InGaN nanowire formation [53].

To understand the complex interaction between the growth conditions and optical properties in InGaN nanowires, macro-photoluminescence spectroscopy serves as a great aid. Fig. 5 illustrates spectra from macro-photoluminescence spectroscopy, indicating a wide

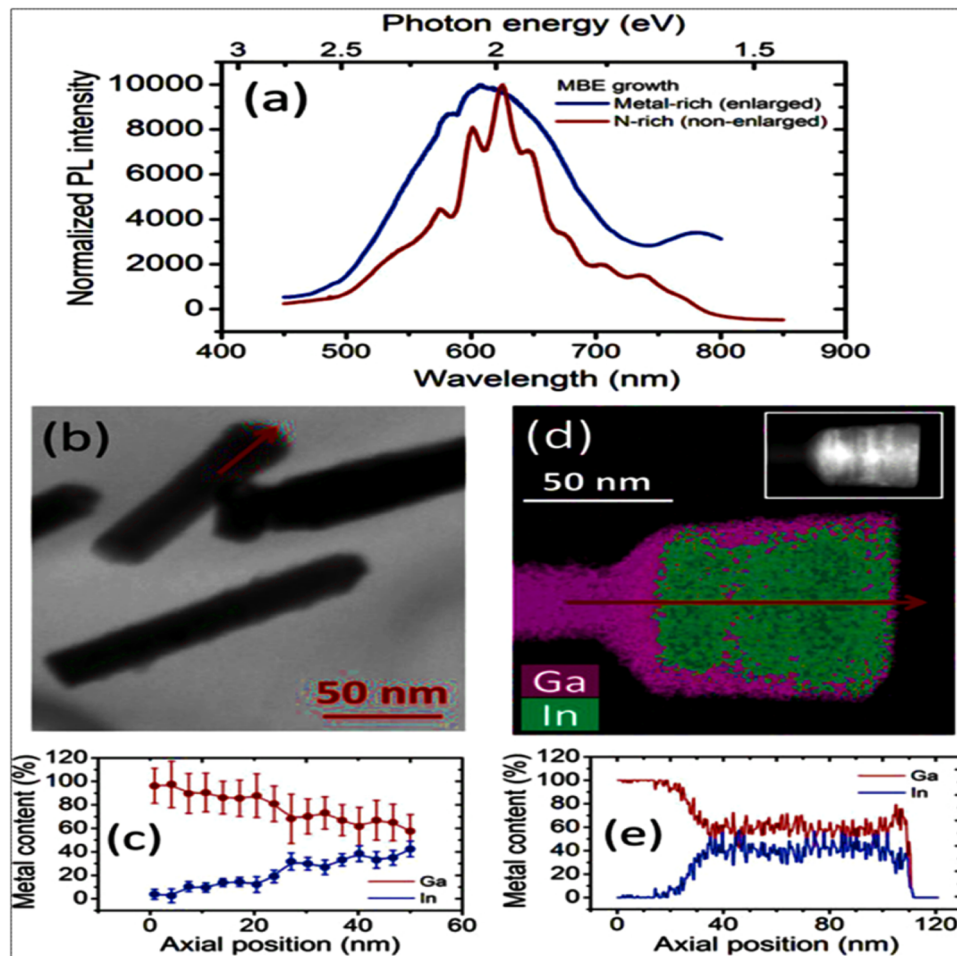


Fig. 5. (a) Macro-photoluminescence spectroscopy spectra of InGaN Nanowires (NWs) emitting in the 600 nm range in nitrogen-rich (non-enlarged) and metal-rich (enlarged) conditions, respectively. (b), (d-inset) Scanning electron microscopy images of InGaN NWs grown in nitrogen-rich and metal-rich conditions, respectively. (d) In and Ga Energy-Dispersive X-ray (EDX) mapping of the InGaN section grown in metal-rich conditions. (c), (e) In and Ga concentration profiles along the growth axis of InGaN NWs grown in nitrogen-rich and metal-rich conditions, respectively [53].

photoluminescence peak width of nitrogen-rich and metal-rich grown InGaN nanowires. These spectra show variations in composition due to different growth conditions. For instance, when it comes to nanowires grown in metal-rich conditions, their chemical composition is thoroughly examined using advanced techniques that include Energy-Dispersive X-ray (EDX) mapping and concentration profiles. Gallium (Ga) and Indium (In) are clearly depicted in Fig. 5(a), and their influence on the photoluminescence peak width is evident in macro-photoluminescence spectroscopy spectra of InGaN nanowires with emission at around 600 nm in nitrogen-rich and metal-rich conditions. In nitrogen-rich and metal-rich conditions, scanning electron microscopy images of InGaN nanowires are represented by Fig. 5(b) and its inset, respectively. An energy-dispersive X-ray (EDX) map of the InGaN section grown under metal-rich conditions is given in Fig. 5(d), which presents a different view. The distribution of In and Ga revealed through energy-dispersive X-ray (EDX) analysis can be seen from Fig. 5(d). Fig. 5 (c) and (e) give concentration profiles of Ga and Indium (In) along the growth axis, thereby comparing nitrogen-rich and metal-rich growth conditions [53]. In materials science, a notable success was accomplished when researchers initiated an epitaxial relationship with substrates to enable the control of material growth along specific directions [54]. In the nanomaterials field, there have been advances in synthesis techniques, peculiar properties (mechanical, optical), and its future uses like drug delivery or sensing [55–57]. In addition, such strides are aimed at making materials that suit different applications [58–61]. The key to this breakthrough is systematic exploration of growth mechanisms,

which can be highlighted by looking at the type of mechanisms involved in detail [62]. For example, nucleation and crystallisation processes were thoroughly examined by the scientists, thus indicating their detailed approach to understanding forces behind materials growth [63, 64].

Nickel-Gold (Ni-Au) catalyst particles play a crucial role in orienting nanowires, as illustrated in Fig. 6. It is noteworthy that various sapphire planes are alternative substrates for the growth of nanowires. Fig. 6(a) and (b) demonstrate that manipulation of Ni-Au catalyst composition can effectively control growth directionality, illustrating the importance of catalysts in this process. This is in line with observations by Tsvion et al. [62], demonstrating controlled nanowire growth on millimeter-long horizontal nanowires across different sapphire planes, as depicted in Fig. 6(c). Growth conditions influence nanowire orientation, as seen in Fig. 6(d). It is obvious from this figure that the preference for the growth direction is almost always perpendicular to the substrate. The latter is promising for improving device efficiency and target features by creating elongated structures of nanowires [62,65]. Looking at crystallographic orientations, the vertical alignment of Gallium Nitride (GaN) nanowires on the (111) oriented silicon brings about simplicity for easy incorporation into devices. The directionality of GaN nanowires' growth on r-plane Al_2O_3 indicates that a catalyst can alter orientation. It also points out the adaptiveness and scalability of NW growth techniques. In the wider context of nanowire growth and semiconductor processes, the choice of the silicon crystal plane like silicon (111) greatly affects the alignment, morphology, and properties of

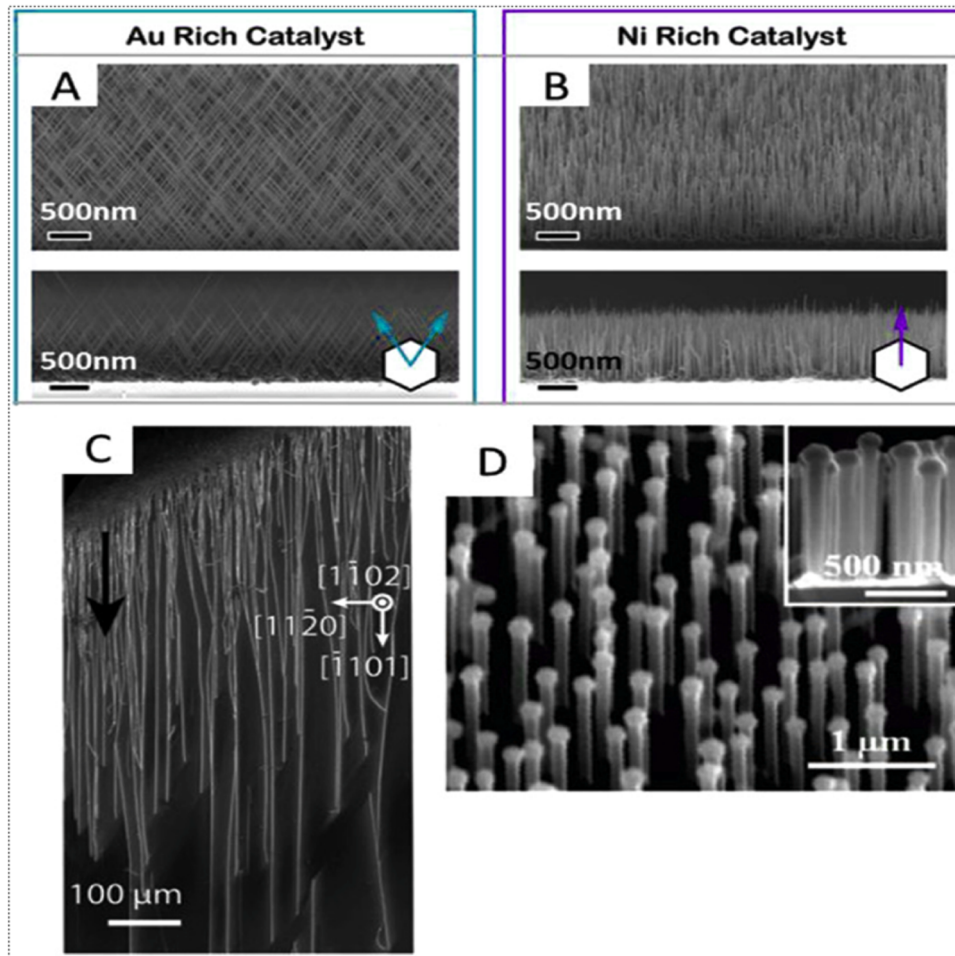


Fig. 6. Illustration of controlled growth directions in GaN nanowires on r-plane Al_2O_3 . Panels (A) and (B) the successful manipulation of growth direction by adjusting the composition of the Ni-Au catalyst particle, Panel (C) millimeter-long horizontal nanowires on different planes of sapphire Panel (D) highlights vertically aligned GaN nanowires grown on Si (111), an orientation often preferred for simplified integration into devices[66].

resulting structures. In certain growth conditions on (111) orientation, there is a well-known preference for vertical alignment, making this particular orientation suitable for some applications in semiconductor device integration. Continuous-flow Metalorganic Chemical Vapor Deposition (MOCVD) has been shown to be useful in generating high-quality Ga-polar nanowires. This detailed study highlights catalysts, growth conditions, and crystallographic orientations intricately interacting to control nanowire structures with designed properties and applications [66,67]. The nature of Ga-polar nanostructures is significantly influenced by the choice of non-nitridated sapphire as a substrate. Ga-polar nanowires have a gallium atom-enriched surface. These are tiny structures, generally in the diameter range of a few nanometers. The MOCVD technology has demonstrated great flexibility in designing GaN/AlGaIn nanowire structures. These involve hybrid combinations of GaN and AlGaIn, the materials that may or may not contain quantum dots. Such structures have certain optical properties at low temperatures [66].

The determination of temperature-dependent optical characteristics for Ga-polar nanowires reveals the intricacies of their stability and reliability for optoelectronic applications, as demonstrated in Fig. 7. Among the particularities of Gallium Nitride (GaN), there is its direct bandgap, which appears to be an important factor for the advancement of micro-LED technology. The optimisation of both optical and electronic properties within GaN nanostructures may be achieved by the use of quantum dots, size-dependent properties, and nanostructured phosphors [66]. The enhancement of micro-LED performance at its core is characterised by merging precise engineering and synthesis techniques, fusing principles from materials science and semiconductor physics [68, 69]. At 5 K, the micro-photoluminescence results observed in Ga-polar nanowires grown by continuous-flow Metalorganic Chemical Vapor Deposition (MOCVD) demonstrate exceptional optical quality. The optical signal clarity and precision are evident, with a peak Full Width at Half Maximum (FWHM) measuring about 0.95 meV. The inset in Fig. 7 (a) further dissects the observed donor-bound exciton and free exciton recombinations. Fig. 7(b) illustrates the microphotoluminescence at low temperature (4 K) of site-controlled GaN/AlGaIn nanowires at different stages, both with and without Quantum Dots (QDs). This is an indirect indication that the chosen growth method can be flexible enough. At low temperatures (40 K), the microluminescence results demonstrate another way to see the versatility of the Metalorganic Chemical Vapor Deposition (MOCVD) technique in making GaN/AlGaIn nanowires that may or may not have quantum dots in specific sites [66]. Therefore, this comprehensive examination of optical characteristics, depending on temperature, will not only extend our understanding of Ga-polar wires but also emphasise their applicability for micro-LED technology [70].

The reason for such progress in micro-LED performance is the integration of accurate engineering and synthesis techniques from materials

science and semiconducting physics. Table 2 presents a comprehensive comparison outlining the effects of materials/substrates, growth conditions, as well as parameters on nanostructure and GaN nanowires (NWs). The influence of diamond and sapphire substrates has been studied using hydride vapor phase epitaxy (HVPE) and metal-organic chemical vapor deposition (MOCVD), shedding light on substrate effects. The table explains the vapor-liquid-solid (VLS) growth mechanism that underpins this through crystallography. Piezoelectric nanogenerators (PENGs) with GaN nanowires (NWs), InGaIn/GaN LEDs, and PEC water splitting enhancement find an array of uses in piezoelectric nanogenerators, LEDs with InGaIn/GaN, and enhanced Photo-electrochemical (PEC) water-splitting.

2.1. Micro-LED technology: categorisation and impact of nanomaterials

Micro-LED technology can transform display capabilities by improving pixel density, resolution, and color. In this context, it is worth noting that the effects of micro-LEDs are most conspicuous in AR/VR systems, including smartphones, wearable devices, televisions, as well as automotive displays. MicroLEDs demonstrate this characteristic by being scalable and can be used for different kinds of devices and display needs. They contribute in terms of precision and flexibility with respect to micro-LED-based devices (1–5 μm) and wearables (5–30 μm), while 5–50 μm -sized displays make phones more detailed and sharper. The larger a device like a television, the better the visual experience it offers (20–80 μm), whereas information is given in detail on an automotive display (50–100 μm). This characteristic is exhibited by digital displays (80–100 μm), highlighting that micro-LEDs can possibly change the entire appearance of the visual environment [2]. In the world of micro-LEDs, it is very important that combinations of different nanomaterials can ensure the proper control of light emission and equipment optimisation [89]. A colloidal suspension of quantum dots uses quantum mechanics principles to show unique optical and electronic properties. This is because they are tiny (<10 nm in size) semiconductor particles that are suspended in a liquid solution. Quantum dots are known for emitting specific colours depending on their size. This has enabled them to improve colour accuracy in display devices. The importance of this feature will be realised when it comes to colour accuracy, since precision in colour representation signifies everything in the displays field. This makes micro-LED displays more flexible when it comes to their colour ranges, which affects the quantum dots' ability to emit light over a wide range of colours as well. Therefore, in addition to being brighter, the latter enables them to generate even more colours that look more true-to-real-life applications [89,90]. Li et al. [90] demonstrates that micro-fluid technology improves display efficiency and expands the spectrum of colours. This is a significant improvement because a QD color conversion layer with 10-micrometer size pixels can enable

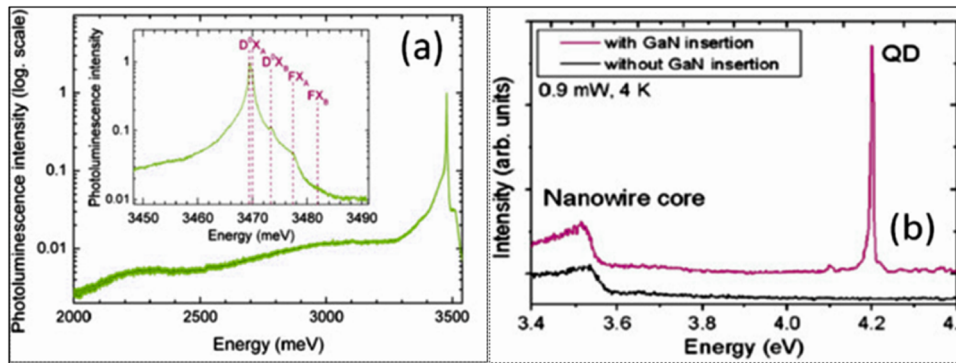


Fig. 7. The exceptional optical quality of the Ga-polar nanowires grown through the unconventional continuous-flow MOCVD approach, (a), A low-temperature (5 K) microphotoluminescence measurement of single GaN nanowires, obtained via self-assembled growth on c-sapphire, reveals a peak Full Width at Half Maximum (FWHM) of 0.95 meV. The inset identifies donor-bound exciton and free exciton recombinations, (b) displays a low-temperature (4 K) microphotoluminescence of single site-controlled GaN/AlGaIn nanowires with quantum dots (QDs) or without [66].

Table 2

A Comprehensive Comparison of Materials/Substrates, Growth Conditions and Parameters, and their Effects on Growth Direction and Performance in Nanostructure and Gallium Nitride (GaN) Nanowires (NWs).

Materials/ Substrates	Growth Conditions and Parameters	Effects on Growth Direction and Performance	References
Semiconductor nanomaterials (nanotubes, nanowires, nanoparticles)	Vapor-liquid-solid (VLS) growth process with metal catalyst on substrate surface	Vertical or random out-of-plane orientations; Nonplanar NWs assembled into planar arrays using post-growth methods or surface- guided growth	[71,72]
Diamond Substrate	Used variable parameters	Substrate impact on GaN NW growth direction and performance	[73,74]
Sapphire Substrate	-Used variable parameters	Substrate impact on GaN NW growth direction and performance	[75]
Silicon Substrate	- Used variable parameters	Substrate impact on GaN NW growth direction and performance	[76]
SiC Substrate	Used variable parameters	Substrate impact on GaN NW growth direction and performance	[77,78]
n-type Si (111) substrate	MOCVD, Au NPs deposition, TMIn and TMGa injection, annealing for catalyst agglomeration, VLS to VS growth mode, GaN/InGaN MQW co- axial NWs	MOCVD, Au NPs, TMIn/TMGa, annealing, VLS to VS growth, Controlled growth direction	[79]
Beryllium oxide (BeO)	VLS approach, Au catalyst, Agglomeration, VLS to VS growth mode, GaN NWs on polycrystalline BeO, Vertical and slantingly aligned NWs, InGaN/ GaN MQW shells	VLS, Au catalyst, Agglomeration, VLS to VS growth, Vertical/slanting NWs	[80]
Graphene-coated monolayer Cu foil	VLS method with Au catalyst, Low- temperature growth, Single-crystal GaN NWs on metallic substrate, Controlled growth direction	VLS, Au catalyst, Low-temperature growth, Controlled direction	[81]
Amorphous glass substrate	Two-step VLS-VS growth method, Au/ In/Ga alloy catalyst, In situ post-growth nitridation, Triangular GaN core NWs, InGaN/GaN MQW shells, Homogeneous growth on glass	Two-step VLS-VS, Au/In/Ga catalyst, Nitridation, Triangular core NWs, Homogeneous growth	[82]
GaN NWs on Sapphire Substrate	MOCVD, VLS growth mode, Pristine and coaxial NWs, SEM characterisation	MOCVD, VLS growth, Pristine/ coaxial NWs, Crystallographic orientation	[83]
GNWs with ZnS Overlayer	MOCVD, VLS and VS growth modes, ZnS shell thickness varied, PEC water-splitting performance, LSV curves, ABPE%, EIS spectra	MOCVD, VLS/VS growth, Varied ZnS thickness, PEC water-splitting performance	[84]

Table 2 (continued)

Materials/ Substrates	Growth Conditions and Parameters	Effects on Growth Direction and Performance	References
Ternary Sandwiched Nanostructure	MOCVD, VLS and VS growth modes, Au NPs sandwiched between GNW and ZnS	MOCVD, VLS/VS growth, Au NPs sandwiched, Improved PEC water-splitting performance	[84]
p-Si (1 1 1)	CVD with metallic Ga and NH ₃ , GaN pre- layer as a self-catalyst	GaN NWs grown on GaN film, NH ₃ flow rates varied (0.4, 0.6, 0.8 L/min)	[85]
Gallium nitride (GaN) NWs	Synthesised using chemical vapor deposition (CVD) on R- and M-plane sapphire wafers with Ni catalyst	Improved electrical and optical performance; Surface-guided growth on different substrates with structural, optical, and electrical characterisation	[54,86]
Surface-guided growth on flat and faceted sapphire surfaces based on a growth model	Kinetics and Growth Mechanism of GaN NWs	Dimensionality of surface diffusion close to 2; Main diffusion pathway is adatom collection from substrate	[87]
NW Synthesis (GaN)	CVD method using Ga ₂ O ₃ powder and ammonia gas on sapphire substrates; Ni catalyst deposition and annealing	Growth on flat R- plane and faceted M-plane sapphire substrates; Wurtzite crystal structure; Axial growth directions	[87]
Thinner GaN NWs on faceted Sapphire Surfaces	Envelope analysis of growth rates versus NW radius; Fitting data to growth model, Morphological differences on annealed M-plane and R-plane sapphire; Thinner NWs on faceted surface	Dimensionality of surface diffusion close to 2 for both substrates; Main diffusion pathway is adatom collection from substrate	[88]

Note- MOCVD (Metal-Organic Chemical Vapor Deposition), VLS (Vapor-Liquid-Solid), VS (Vapor-Solid), NPs (Nanoparticles), TMIn (Trimethyl Indium), TMGa (Trimethyl Gallium), MQW (Multiple Quantum Well), Au (Gold), BeO (Beryllium oxide), GNW (GaN Nanowire), ZnS (Zinc Sulfide), PEC(Photoelectrochemical), LSV (Linear Sweep Voltammetry).

high-resolution, full-color active-matrix micro-LED displays [91]. The significance of GaN micro-LEDs display in this area is due to their brightness as well as the ability to modulate each pixel individually [92]. Wu et al. [93] brought out submicron-scale LEDs, which could be used in emerging optoelectronics, especially virtual and augmented reality [15, 93]. In Fig. 8(a), this involves creating a core-shell structure and distinct morphology of InGaN-InGaN/AlGaN multiple-quantum-well nanowire array. Fig. 8(b) demonstrates that a similar peak wavelength of electroluminescence is maintained even with a one-order magnitude change in current injection, indicating the stability of the micro-LED at different operational conditions. Fig. 8(c) depicts the current-voltage characteristics that further illustrate practical application and performance [93]. Nano-grooves have been used to push the limits of micro-LED technology, highlighting the importance of nanostructures in this field [94]. Multidimensional advances in the domain of micro-LEDs can be seen through the convergence of mixed technologies and materials, indicating a future where screens will not only have sharper images and better color accuracy but also work with different applications on various devices.

Lozano et al.'s analysis of the ability of metal nanostructures to

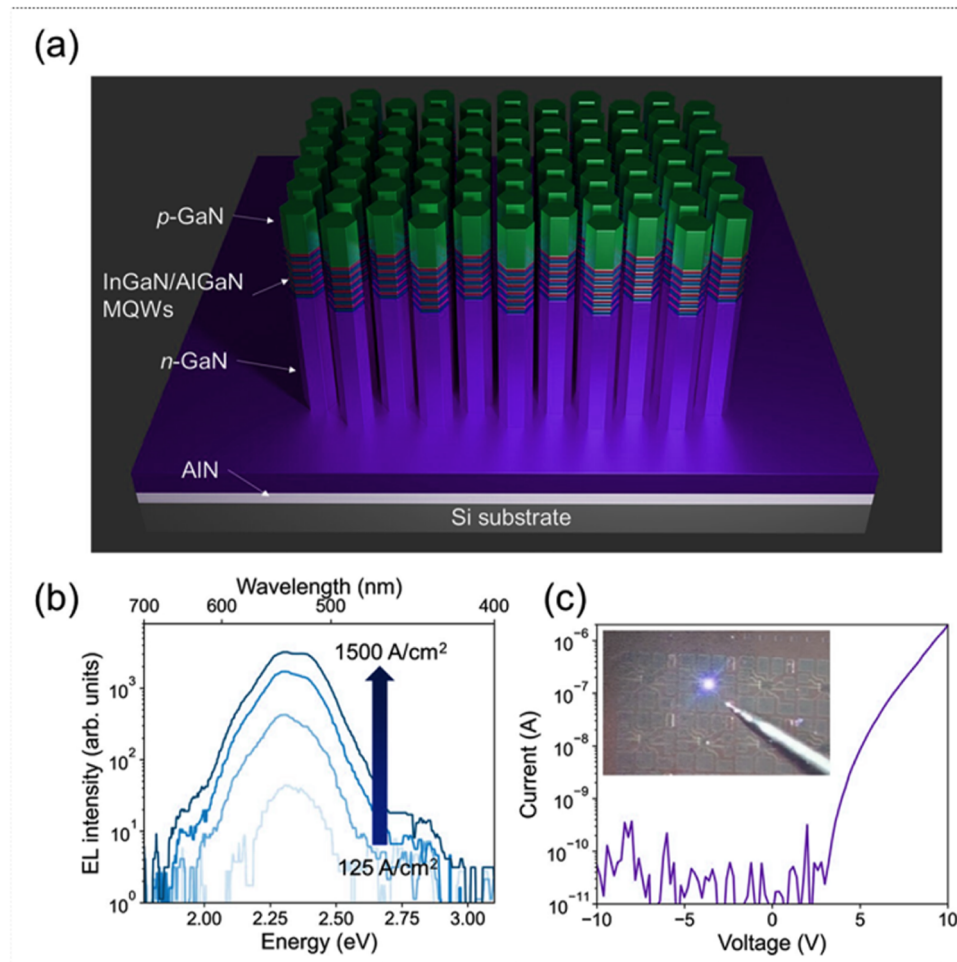


Fig. 8. (a) Schematic representation of the InGaN/AlGaN multiple-quantum-well nanowire array, illustrating the core-shell structure and unique morphology, (b) Graph depicting the consistent peak wavelength of the device's electroluminescence over a one-order-of-magnitude change in current injection, highlighting the stability of the μ LED. (c) Current-voltage characteristics of the device exhibiting a strong green emission visible to the eyes and a good rectification ratio, emphasising the practical application and performance of the developed μ LEDs[93].

control light emission dynamics is discussed in [95]. David et al.'s [96] introduction of coinage metal nanoclusters (MNCs) is a harbinger of innovative chromophores – a sign that they could have their place in cutting-edge lighting technologies. Sung et al. [97] demonstrates enhanced electroluminescence in GaN-based LEDs decorated with metal nanoparticles, marking the dawn of photon scattering and coupling as never before. Zhang et al.'s [99] investigation of tin dioxide nanoparticles provides insight into the Schottky barrier height adjustments at the Ag/p-GaN interface, which is an important contact for various electronic and optoelectronic applications where its properties affect device performance [98]. Zhu et al.'s [100] study on merging graphene films with single-walled carbon nanotube structure presents a unique material combination with improved optical and electrical characteristics and new possibilities for application in transparent conductive films [98,101]. The nanoparticles of metallic oxides demonstrate versatility, suggesting their importance in advancing display technology [102,103]. The contest for the highest colour performance and efficiency has given birth to several innovations in projection displays. There is a new theoretical model, the Quantum-Dot Color Wheel (QD-CW), which breaks the duty cycle ratio and transcends color purity and gamut range constraints [104]. The duty cycle ratio is a measure of the time a particular color spends on display in a color wheel or similar device. For QD-CW, breaking the duty cycle means changing or surpassing the classical time division among colors on the wheel [105,106]. To optimise the QD-CW device for better white balance, core-shell CdSe@ZnS

Quantum Dots (QDs) were examined with respect to absorption and photoluminescence spectra [107,108]. The QD-CW device was fabricated using precise photolithography; consequently, it exhibits uniformity as well as smoothness in Quantum-Dot Color Conversion Layers (QDCCs) [109,110]. Blue light leakage got eliminated, and light conversion efficiency increased as a consequence of incorporating up-to-date Distributed Bragg Reflector (DBR) technology into the structure [111,112]. When integrated into the display prototype, the QD-CW device expands the color gamut area compared to conventional color wheel systems through a complete cycle by ensuring synchronous blue, green, and red-light output [113]. Intriguing results were discovered during research on colour chromaticity change in liquid crystal display light guide panel that was caused by wavelength-dependent scattering. The incorporation of different nanomaterials into micro-LED displays provides an attractive way to improve performance, which includes alternative and innovative things related to the present demands of current exhibit technologies [114]. LEDs emit white light at the edge of the light guide, which is dispersed by the patterned dots at the bottom of the panel. Spatially, lights that are far away from LEDs appear to be reddish, while for those closer to LEDs, they have a bluish color. The color differences demonstrated, as quantified values, range between 0.01 and 0.06, emphasising the role played by light guide resin and thickness [115]. The Lambertian distribution of Mini-LEDs was employed to come up with a solution using a nonuniform quantum-dot color conversion film (QDCCF). Experimental results confirmed a

considerable improvement in the uniformity of luminance (up to 89.91%). This was explained through ray tracing simulations. By enabling good white balance, high uniformity (92.15%), and a wide color gamut (109% NTSC), the application of the nonuniform QDCCF on the prototype gives hope for further progress in Mini-LED-based display technology [116]. The use of quantum dots, nanowires, metallic nanostructures, metal oxide nanoparticles, and graphene-based materials in micro-LED displays promises to improve their performance and visual vibrancy in the future.

3. Fabrication and integration of micro-LEDs for display applications

Manufacturing processes of Micro LED displays can greatly benefit from the use of advanced materials like polymer nanocomposites and graphene-based fibers [117,118]. Their performance is likely to be improved in numerous ways, including increased robustness, resilience against EMI and flexibility [119]. The integration will enable Micro LED technology to perform more reliably across various electronic devices [120]. Epitaxial growth is critical in defining the micro-LED crystalline structure, serving as the foundation of the technology [17]. The size and structure of each individual micro-LED are defined by lithography and etching, which are essential steps in ensuring accurate craftsmanship. Nevertheless, these difficulties become more pronounced with a decrease in lateral dimensions, leading to an "efficiency cliff" [121,122]. Micro-LED production is a combined collaboration of two major methods of manufacturing: top-down and bottom-up techniques [123]. The conventional top-down processing using plasma etching mainly exacerbates the efficiency cliff through surface damage as well as crystal defects [124,125]. To improve surface stoichiometry and device characteristics, various strategies, including thermal annealing and N_2 plasma exposure, have been adopted to suppress plasma etch-induced damage [126]. Aiming at surface passivation [127,128], post-mesa etching surface treatments such as atomic layer deposition (ALD) and $(NH_4)_2S$ treatment have been used. This shows that post-mesa etching treatments of the semiconductor surface such as atomic layer deposition (ALD) and $(NH_4)_2S$ treatment are used to improve the properties of the semiconductor material for electronic device fabrication. Post-mesa is a term used in semiconductor fabrication to describe the processes and treatments given to a semiconductor device following mesa etching. It is a method where isolated regions or mesas are created on a silicon wafer. These mesas define the active areas of various devices, such as transistors or diodes. Wet etching using KOH eliminates the leakage paths created during plasma etching, providing an idea about the depth of the crystal damaged by plasma [127–130]. Another important aspect is hydrogen plasma treatment, which deactivates Mg acceptors around the device mesa, raising peak EQE in green micro-LEDs [131]. Solutions to the lattice mismatch in InGaN alloys between GaN and InN include V-pits, n-GaN buffer layers, superlattices, pseudo-substrates, and Smart Cut™ technology [98,132–134]. It has been demonstrated that V-pits, thick n-GaN buffer layers, superlattices, and partial decomposition of InGaN underlayers are effective approaches to address the issue of efficient red emission. This may allow EQE to go beyond 1%, although this strategy is still faced with challenges involving defects [98, 132–134]. V-pits, V-shaped recesses that can form on the surface of GaN semiconductors during growth, are part of strategies to optimise the performance and reliability of GaN-based devices, such as high-electron-mobility transistors (HEMTs). Integrating multi-color micro-LEDs into high-resolution displays poses challenges, especially in achieving smaller LED chip sizes [135,136]. Both passive and active-matrix configurations present hurdles in pick-and-place mass transfer, defect identification, testing, and yield concerns [15]. Precision in the top-down technique involves the deliberate removal of larger materials, whereas the bottom-up method builds up structures layer by layer. The detailed discussion of top-down and bottom-up approaches can help understand more about the production of micro-LEDs.

Developing ways to handle plasma etching challenges and InGaN alloy growth, coupled with advances in post-mesa etch treatments, can be considered important for improving micro-LED display technology moving forward.

3.1. Top-down approach in micro-LED patterns

In the realm of display technologies, the most important thing is to make micro-LEDs as accurately as possible using the top-down approach, where lithography, etching, and deposition are combined in an intricate manner. Lithography reveals the microLED pattern blueprint, which serves as a starting point for the complex manufacturing process [137]. The rest of the actions heavily rely on this accuracy-based step, as it determines microLEDs' properties and layout. Micro-LEDs are designed with precision via etching, a particular step in the top-down approach that engraves them meticulously by taking some material away from them, defining appropriate structures [138]. Micro-LEDs are thus sculpted with accuracy through this painstaking process to ensure optimal performance. Meticulous deposition is used to introduce depth into micro-LED structures and strategically place semiconductor and contact layers that would enhance their overall functionality [139]. This forms part of the structure that enhances the performance of these systems. In lithography and etching, as they engage in an iterative dance of refinement, micro-LED features are fine-tuned to closely match the desired specifications [140]. This means that through this process, every micro-LED is made to meet the exact design criteria. After that, there is a crescendo in the top-down approach, which is the deposition of metal contacts that help to form electrical connections essential for seamless communication within the micro-LED array [141]. This is the last stage of fabrication, and it guarantees that the micro-LED array will perform its functions. The precision required for advanced display technologies is encapsulated in the top-down approach used in micro-LED fabrication, which is likened to a symphony of technology [123]. This pushes the boundaries of visual display capabilities, and each step contributes to the synergy of processing and technology. It starts with lithography and ends with metal contacts deposition [137].

3.2. Bottom-up approach in micro-LEDs with precision

The landscape of micro-LED fabrication has evolved, with both top-down and bottom-up approaches demonstrating technological advancements. SAG, a technique allowing direct growth of micro-LEDs on the substrate, is the cornerstone of the bottom-up approach [142]. For instance, this process fine-tunes indium concentrations in InGaN platelets to control colors on displays. It seems SAG could be the way to go for precise fabrication of micro-LEDs. Self-assembly and quantum dot integration, which are underpinned by SAG, are among bottom-up techniques that promise displays with resolutions as high as possible, with unbeatable precision. However, they have their fair share of challenges. It is extremely challenging to consistently retain quality assembly at large scales. Bottom-up approaches must be handled delicately to avoid having this approach become too expensive in terms of production costs. The choice between mass transfer for larger displays and monolithic integration for smaller ones becomes critical in the light of the ongoing development of Mini-LED and micro-LED displays [143]. It is important to note that this decision determines the scalability, cost-effectiveness, and overall success of micro-LED displays in the market. A paradigm shift in visual experiences is heralded by this, as diverse applications are integrated into the future, where micro-LED displays seamlessly integrate with each other – made possible by the complex interplay between top-down and bottom-up approaches [144]. The comprehensive exploration of fabrication techniques is facilitated through the coexistence of these approaches, thus leading to the development of micro-LED technology. A bottom-up approach can provide higher precision and resolution in micro-LED displays, for

example, through techniques such as SAG.

3.3. Mass transfer and monolithic integration

Epitaxial growth, which is a key element, shapes crystalline structures, leading to the development of micro-LEDs [145]. Processes such as lithography and etching represent the measurements of each micro-LED's size and structure, guaranteeing accuracy in performance. Nevertheless, these difficulties become more pronounced with a decrease in lateral dimensions, leading to an efficiency cliff [124,125]. Micro-LED production benefits from both top-down and bottom-up approaches. Conventional top-down processes, such as plasma etching, worsen the efficiency decline by leading to damage of the surface and crystal defects [125,146,147]. Approaches like N_2 plasma exposure or thermal annealing may help mitigate plasma etch-induced damage [126]. Some examples of such post-mesa etching surface treatments include atomic layered deposition (ALD) and chemical treatment, which are used to passivate surface states [127,128]. Identification of defects and mass transfer challenges has made the mass transfer of Micro LEDs, both in active-matrix and passive-matrix, more complex, thereby requiring testing as well as addressing yield concerns [15]. In this case, the interplay of top-down with bottom-up approaches is a pointer to a future where micro-LED displays will fuse into different applications, leading to a breakaway from traditional visual encounters [145]. This is seen through the coexistence of both methods in making various fabrication techniques that help in advancing micro-LED technology. Micro-LED technology advances mass transfer and monolithic integration. However, there are major challenges that have been solved by introducing innovative solutions. For example, PDMS stamp transfer of X-Celeprint and laser transfer method of SONY. Fig. 9 presents the two models of mass transfer and monolithic integration illustrated schematically [148].

3.3.1. Micro-LEDs through monolithic integration

Paradigm shift in micro-LED technology can be seen through monolithic integration, whereby epitaxial wafers undergo a metamorphosis, altering their form into intricately arranged chip arrays. These arrays perfectly couple with color conversion materials that result in unmatched precision and vibrancy of display effects [145]. The array of uses for monolithic integration is highlighted to include the integration of InGaN/GaN LED (Light Emitting Diode) chips with vertical MOSFETs (Metal Oxide Semiconductor Field Effect Transistors) or AlGaIn/GaN HFETs (High Electron Mobility Transistors). This requires combining optoelectronic devices and those that are electronic on a single substrate of semiconductor, such as in micro-LED technology, which aims at developing small-sized and multifunctional semiconducting devices for power-efficient and optoelectronic systems [94,

149,150]. As a result, there is an expansion of opportunities through a fusion offering better functions and properties in the integrated systems. A groundbreaking development in this field is Zhang et al.'s [151] pioneering fusion of monolithic integrated device (MID) technology with Micro LEDs [152], which creates new frontiers in display technology. InGaN/GaN LED chips are the most commonly used LED materials due to their high light emission efficiency and comprise Indium Gallium Nitride (InGaIn) as its active layer and the semiconductor element made up of Gallium Nitride (GaN) [70,151]. When combined with Moulded Interconnect Device (MID) technology—where electronic circuits are directly integrated into 3D moulded plastic components—it opens up possibilities for advanced and compact LED applications. This is because MID technology allows for intricate and space-efficient designs, thereby enabling the seamless integration of InGaIn/GaN LED chips into different products and devices. Consequently, this integration pushes the industry to never-before-reached heights, which promise revolutionary advances in micro-LED displays' abilities [152]. The transition of micro-LEDs via monolithic integration implies a drastic change in display technology by bringing together several technologies, such as InGaIn/GaN LED chips and MID technology, to expand the realm of possibilities.

3.3.2. Moulded Interconnect Device (MID) technology

Moulded Interconnect Devices (MIDs), the leading edge of modern electronics, combine seamlessly with micro-LED integration, initiating a pivotal shift in display technology [153,154]. Landmarks in electronic manufacturing include the use of Leneke et al.'s [155] revolutionary 3D-MID [156] multilayer process that is redefining the limits of micro-LED can be achieved [154]. Moser et al.'s [144] exploration of Moulded Interconnect Devices (MIDs) in automotive applications highlights the potential for integrating all functionalities onto a single chip. It indicates a path wherein advanced display abilities in cars may be turned into reality, leading to not only better fuel consumption but also a much-improved user experience [157]. These types of integration change the whole system and are instrumental in improving the system integration that leads to efficiency gains, as well as more streamlined electronic architectures, thereby enabling the optimisation of electronic systems [156]. The seamless integration of MIDs with micro-LEDs is a merger that goes beyond the scope of electronics manufacturing and represents an important milestone in display technology. The display landscape is being transformed by mass transfer, monolithic integration, and MID technologies, which are being leveraged by micro-LED. This has been well demonstrated by the achievement of monolithic micro-LED displays on gallium-nitride-on-silicon substrates [154]. It further emphasises its versatility and adaptability to different display technologies, marking a significant step forward in the sector. In addition, manufacturing complexity, cost-effectiveness, and uniformity in large displays are not going to be easy ones to crack, being the key issues

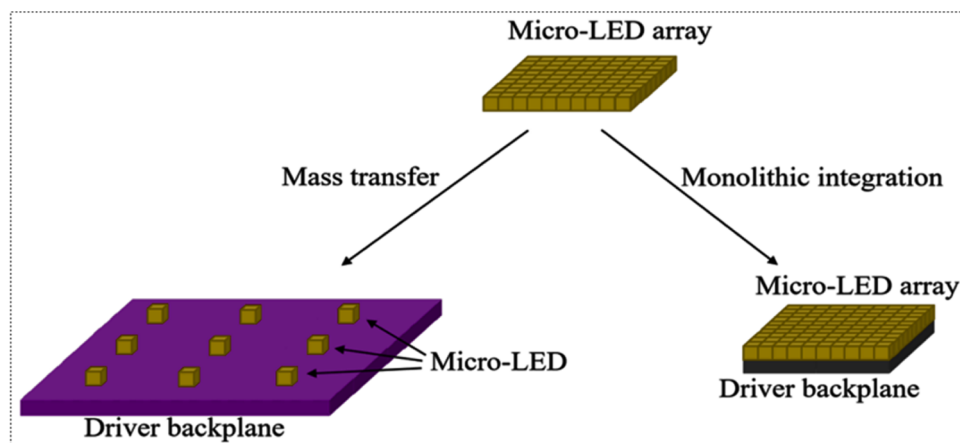


Fig. 9. Schematic diagram of two methods of mass transfer and monolithic integration [148].

of concern that demand close attention [154]. To address these challenges, there is a need for collective input from researchers, engineers, and industry players. Despite that, MIDs technology integration with micro-LEDs can improve display performances. The technological integration of both MIDs and micro-LEDs not only deals with the current problems but also provides new possibilities for better performance, efficiency, and feasibility in display applications [156–158]. However, critical challenges, including manufacturing complexity, cost-effectiveness, and uniformity in large displays, loom as pressing agendas that require meticulous attention [157].

4. Efficiency enhancement through nanomaterials and III-nitride nanostructures

Notably, recent research endeavors have improved the efficiency of micro-LEDs in the context of small-area blue InGaN micro-LEDs [46]. However, progress has been made to enhance efficiency in green and red micro-LEDs; yet, these developments underscore the importance of understanding efficiency cliffs in tiny devices [159]. LED efficiency evaluation is a widely used conventional approach that revolves around the external quantum efficiency (EQE), which has a direct relationship to light extraction efficiency (LEE) and internal quantum efficiency (IQE). Injection efficiency is another important parameter in this regard. For example, high LEE is a challenge for III-nitride LEDs since it arises from fundamental problems in extracting emitted light from semiconductor layers. This complication results from total internal reflection at GaN/sapphire interface or any other LED mechanism that causes light absorption [160]. The GaN/sapphire interface is the point where Gallium Nitride (GaN) merges with a sapphire substrate in LEDs, and this is the cause of complications in light extraction. Light trapping techniques are applied with a lot of thoughtfulness to optimise light extraction efficiency, lowering total internal reflection and thereby enhancing the likelihood that photons will slip out of the LED's structure, hence improving luminous output. Several mechanisms contribute to light absorption in an LED, including interaction with the active material (such as InGaN), absorption by different layers in the LED structure, and possible losses due to reflector or absorber materials inside the device [161]. The high refractive index of GaN ($n_{\text{GaN}} \sim 2.5$) is particularly responsible for this absorption challenge, which leads to total internal reflection at the GaN/sapphire interface, light trapping and multiple mechanisms within the structure which contribute to light absorption in LED structures as described in Eq. (1) [162].

$$\text{EQE} = \eta_{\text{inj}} \times \text{light extraction efficiency} \times \text{internal quantum efficiency} \quad (1)$$

Optoelectronics and high-power electronics have been significantly impacted by the development of AlN, GaN, and InN Group III nitride semiconductors. High-brightness visible LEDs are made possible by these semiconductors, which have revolutionised LED-based full-color displays to serve a wide range of purposes. In 1994, a major breakthrough was achieved in designing highly efficient blue LEDs, which proved their usefulness in various fields [163]. The ability to tune the bandgap makes ternary alloys like AlGaIn, InGaIn, and AlInN, and structures extremely versatile. These ternary alloys usually consist of Aluminum (Al), Gallium (Ga), Indium (In), and Nitrogen (N). Such ternary alloys with gallium nitride (GaN) as a representative are common in semiconductor materials, especially for the development of compound semiconductors. Literature often emphasises the importance of crystallographic polarity, especially for GaN deposited by plasma-assisted molecular beam epitaxy (PIMBE) on c-plane sapphire GaN/sapphire substrate, for hetero-epitaxial growth in III-V nitride semiconductors [45]. Difficulties in extracting emitted light from the semiconductor layers result in challenges in achieving high LEE in III-nitride LEDs. LEE is a key parameter for optimising the performance of III-nitride-based optoelectronic devices, like light-emitting diodes

(LEDs), where it ensures that light is effectively extracted from the device. Therefore, it is important to improve LEE to achieve high brightness and energy efficiency of III-nitride LEDs [160]. Revolutionary micro-LED technology depends on the efficiency of III-nitride materials, like GaN, concerning its power conversion applications. In this sense, GaN-based power electronics can enhance energy conversion as well as increase the driving range in electric vehicles. Some of these methods include flip-chip and roughening the nitrogen face of n-GaN, which disrupts total internal reflection [164]. Fig. 10 represents that light extraction efficiency has changed over the years [165]. The micro-LED technology is revolutionised through the increase in efficiency, which is driven by III-nitride materials, particularly GaN. Moreover, power conversion in different sectors has been transformed by GaN-based devices for power electronics that improve energy conversion and extend electric vehicle range. Furthermore, III-nitride materials optimise the power conversion efficiency of solar power systems, thus enhancing the general performance of renewable energy sources. Consequently, the importance of III-nitride materials extends to various technologies, such as Lin et al.'s [166] work on UV detectors used to purify water and air [166].

The high-frequency and high-power electronics domain witnesses the importance of III-nitride materials, particularly InN, in driving developments in high-speed wireless communication systems, including but not limited to 5 G technology and beyond [167]. The race to increase the efficiency of micro-LEDs has led to research in new fabrication methods that are expected to overcome hindrances and fully exploit these revolutionary technologies. It is a method of improving efficiency through the use of PA-MBE and a Ti mask layer to pattern nanowire arrays on Ga-polar GaN-on-sapphire substrates. Here, GaN is a WBG semiconductor material that is grown on a sapphire substrate with a Ga-polar orientation, predominantly containing gallium atoms at the surface. PA-MBE has employed a plasma source from which it derives ionisation energy needed to undertake the deposition of GaN layers accurately, thus regulating growth conditions and properties of materials. The use of titanium (Ti) mask layer is one way of differentiating specific areas for epitaxial growth according to [168] thereby enabling development of complicated structures and better control overgrowth process of GaN on sapphire. This procedure is used in the production of GaN-based devices, such as high electron mobility transistors and LEDs, where the performance is optimised based on substrate orientation and controlled growth [169]. Results obtained by using high-resolution transmission electron microscopy (TEM) of the nanowires reveal an AlGaIn shell, which can reduce surface recombination. The hole injection is further enhanced by an n++/p++ GaN tunnel junction contact layer, where there are heavily doped n-type (n++) and p-type (p++) regions during the process. As a result, green electroluminescence (EL) of micro-LEDs with high stability was realised, which had a maximum external quantum efficiency (EQE) of about 5.5% at 3.4 A/cm². The wavelength shift for semi-polar facets decreases as a function of

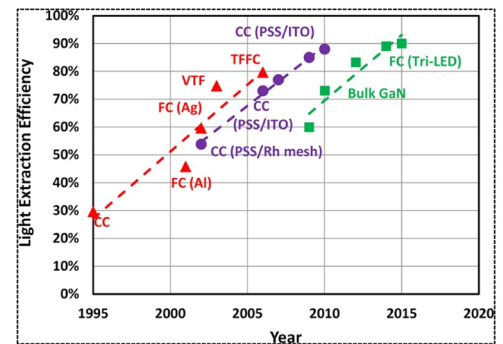


Fig. 10. Evolution of light extraction efficiency over time (FC: Flip-Chip, VTF: Vertical Thin Film, PSS: Patterned Sapphire Substrate, ITO: Indium Tin Oxide) [165].

injection current [170,171]. Enhancements in carrier injection and higher growth temperatures are found by the investigation of N-polar nanostructure-based LEDs. These nanowires have flat tops, and a layer of Aluminum Gallium Nitride (AlGa_N) surrounds or encapsulates another material, protecting the active region from surface recombination. It implies an outer layer, and it's indicated by the composition of AlGa_N, a ternary alloy of Aluminum (Al), Gallium (Ga), and Nitrogen (N) [172, 173]. As a result, N-polar micro-LEDs exhibit an EQE of ~11% at 0.83 A/cm². The faceted nature of the InGa_N active region can be seen in transmission electron microscopy (TEM) images, where there are facets on the active region composed of Indium Gallium Nitride (InGa_N). Thus, the faceting promotes excitonic recombination, hence improving device efficiency [174,175]. Further examples include sub-micron LEDs emitting red light made using a thick InGa_N segment, in addition to the successful N-polar green LEDs. The intensity of emission is enhanced via in-situ annealing, such that these devices exhibit higher efficiency than their counterparts from the top-down method [176,177]. The addition of an InGa_N/Ga_N Short-Period Superlattice (SPSL) also further red shifts the emission, contributing to improved EQE and wavelength peak values. In the case of InGa_N/Ga_N, this short-period super-lattice relates to a periodic arrangement consisting of alternating thin films comprising Indium Gallium Nitride and Gallium Nitride [178,179]. Achieving high-efficient long-wavelength light-emitting diodes (LEDs) is challenging because of low spectral purity. The effort to narrow down the emission spectrum is ongoing, as scientists experiment with photonic crystals in creating optical microcavities [179,180]. Inclusion of photonic crystals in micro-LEDs leads to Photonic Crystal (PhNC) devices that exhibit stable emission peaks and less efficiency droop. This development has the potential of solving challenges related to spectral impurity. Photonic crystals allow for light control by the use of periodic structures, hence enabling meticulous tuning of emitted characteristics within micro-LEDs. Improved spectral purity is inferred from the stability of emission peaks, while less efficiency droop shows more stable operation at high power levels [178,179]. The same technological advances are evident with nanocrystal surface-emitting lasers (NCSELs). For these devices, complex DBRs are not necessary, and they can also be used in selective area growth, demonstrating their flexibility even if they do not work well under high power. They would be applicable in communication, projectors, displays, and optical storage technology once packaged properly, and their thermal needs are managed [181–183]. For better performance of micro-LEDs, nanostructures, especially III-nitride, should be incorporated [184,185]. The integration of nanomaterials, such as III-nitride nanostructures, on the other hand, enhances the performance of micro-LEDs [186]. This integration supports further development of Micro LED technology that will enable flexible and transparent micro-LED displays, which would represent a significant leap from current display technologies [185]. Slimane et al.'s [187] study is a full inquiry into this transformative period of time, stressing III-Nitride micro and nano structures for solid state lighting. The need to explore advanced materials, innovative fabrication techniques, and improved substrate designs still exists, despite historical methods, including coating blue LEDs with yellow phosphor or using RGB LED combinations to achieve white light in LEDs [187]. Wierer et al. [188] examines the operational challenges unique to micro-LEDs that require further research to improve their performance and reliability [189]. Fig. 11 illustrates that III-nitride materials have transformed conventional wisdom, leading to the development of brighter and energy-efficient white light sources [190]. Advancements in III-nitride technology have brought forth highly efficient blue LEDs, which have revolutionised lighting, leading to more sustainable and visually improved lighting solutions. This integration of III-nitride materials exemplifies their influential role in shaping the course of modern technology with broad implications for various applications in electronics, optoelectronics, etc [191–193].

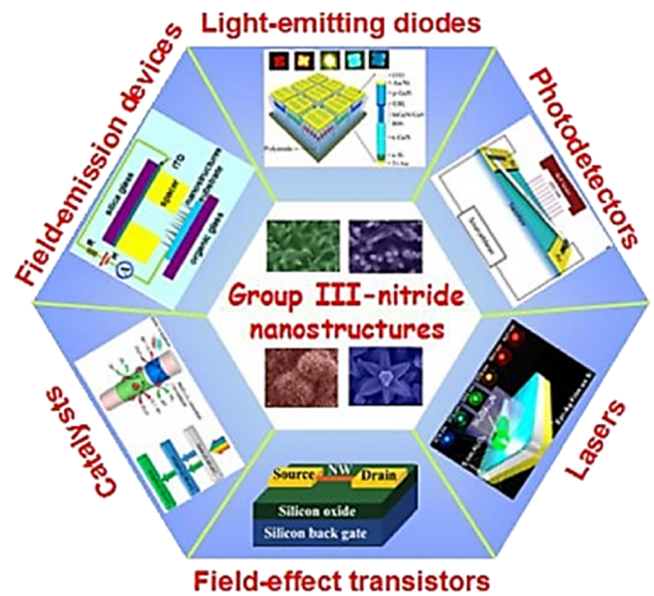


Fig. 11. Revolutionising Illumination: Blue LED Technology and III-Nitride Semiconductors [190].

5. Nanomaterial integration and III-nitride nanostructures in micro LED display applications

The progress in the development of GaN-based LEDs (Light Emitting Diodes) using gallium nitride (GaN) has been remarkable, contributing to the exceptional performance capabilities of GaN-based LEDs. GaN, a wide-bandgap semiconductor material, has played a pivotal role in advancing LED technology, particularly in the domain of solid-state lighting [194]. For efficient light emission, there has been a significant boost in GaN material improvement, which has led to its highly desirable properties. There has been great improvement since the introduction of advanced InGa_N heterojunction and quantum-well LEDs that have significantly improved brightness and efficiency [195]. These devices are key components in many applications due to their high brightness and efficiency, which result from continuous optimisation of materials as well as design principles employed. Fig. 12 illustrates the basic elements and operating principles of a double heterostructure LED [165]. The schematic includes key elements such as the substrate, epitaxial layers, and the process of electron and hole recombination in the active region, ultimately resulting in the emission of photons.

The motivation behind achieving with III-nitride LEDs was a way to find out if it was possible to come up with a true-blue LED, which was later realised in the 1990 s. The breakthroughs of the 1980 s that included Metal-Organic Chemical Vapor Deposition (MOCVD)-assisted growth of high-quality GaN-on-sapphire opened the avenue for uniform and high-crystallinity GaN films fabrication. This solved compatibility issues among direct bandgap semiconductors [196]. Fig. 13 illustrates important milestones in the history of development of the first GaN-based light-emitting p-n junction diode [39].

Though several different types of quantum dots are used, indium gallium nitride (InGa_N)-based quantum dots integrated into III-nitride nanostructures, especially in InGa_N-based nanomaterials, have aided a lot in the development of Micro LED technology [197]. This exploits size-dependent quantum effects to improve optical properties and broaden color gamut while enhancing color accuracy. To achieve efficient red emission on InGa_N/Ga_N-based LED chips, some novel approaches such as SU-8 bonding and aerosol jet printing with QDs ink have been developed [47,198]. Fig. 14 provides an extensive description of micro-LED technology that includes full-colour display emissions, precision of aerosol jet printing, optical configurations for projection systems, and intricate nano-pillar LED structures. Fig. 14(a) shows the

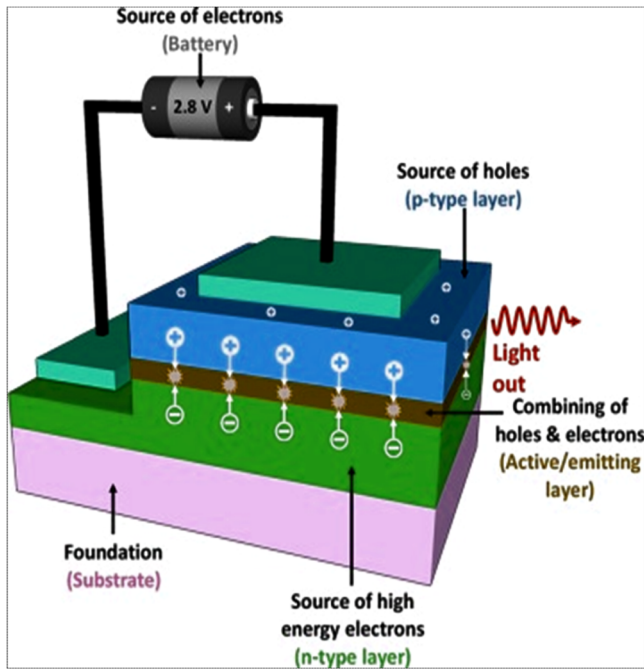


Fig. 12. Simplified illustration of a double heterostructure LED, providing a visual representation of its fundamental components and operation [165].

emission of full-colour sub-pixels under a microscope [199]. Fig. 14(b) illustrates the aerosol jet printing technology [199,200]. Fig. 14(c) demonstrates an optical setup for a projection system [199,201]. Lastly, Fig. 14(d) shows the methods and electron microscope view of embedded nano-pillar LED creation [199,202].

Micro-LEDs take precedence in terms of picture quality and a

reduced risk of problems such as screen burning compared to OLEDs (Organic Light-Emitting Diodes). Nonetheless, the manufacturing process of larger micro-LED screens may be complex and expensive. Due to their flexibility acknowledged in literature and designs, OLEDs are mostly preferred for applications requiring design flexibility [203]. Fig. 15 provides an overview of three common device setups in display technology that emphasise different technologies employed for achieving evolving full-colour images. Fig. 15(a) exhibits RGB-chip emissive displays, which use single micro-LED (μ LED) or OLED chips as subpixels. Displays and lighting applications utilise two distinct technologies: Micro-LEDs (μ LEDs) and OLED (Organic Light-Emitting Diode) chips. In this setup, these subpixels also emit light, making up the total display output. Fig. 15(b) outlines the concept of colour conversion (CC) emissive displays. Here, either micro-LEDs, micro-LEDs, or OLED chips can be used as subpixels with color conversion mechanisms implemented. The diagrammatic representation exposes the methods applied to generate full-colour images through means of colour conversion processes. Non-emissive display technology is represented by μ LED Backlit LCDs in Fig. 15(c) [204].

This kind of convergence has been a focus in several research works that underline the importance of optimising micro-LED performance and functionality [168,205,206]. Much interest in this is to improve light extraction efficiency, control emission angles, as well as polarisation management. This integration creates the breakthroughs of the micro-LED technology by combining nanomaterials with III-nitride nanostructures [152]. Nanomaterials, like gallium nitride (GaN), are very significant substances not only in micro-LED technology but also for the development of solar energy absorption and energy conversion improvements in the renewable energy industry [207]. The use of nanomaterials in micro-LED technology is based on Molecular Beam Epitaxy (MBE) and Metal-Organic Chemical Vapor Deposition (MOCVD) techniques [208]. This symbiotic relationship between nanomaterials and processing methods, particularly GaN and III-nitride nanostructures, enhances the efficiency of optoelectronic gadgets. These

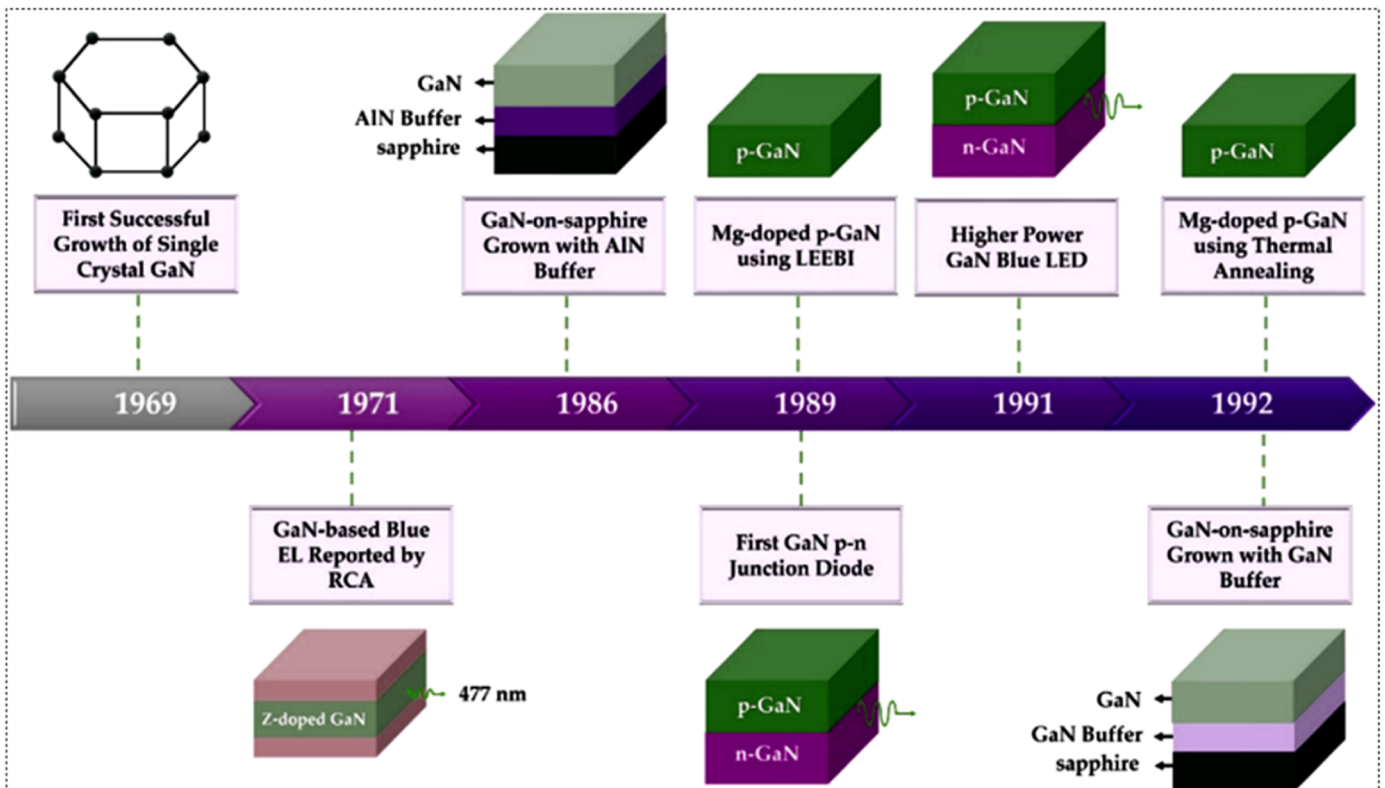


Fig. 13. Timeline of important milestones achieved in the development of the first GaN-based light-emitting p-n junction diode [39].

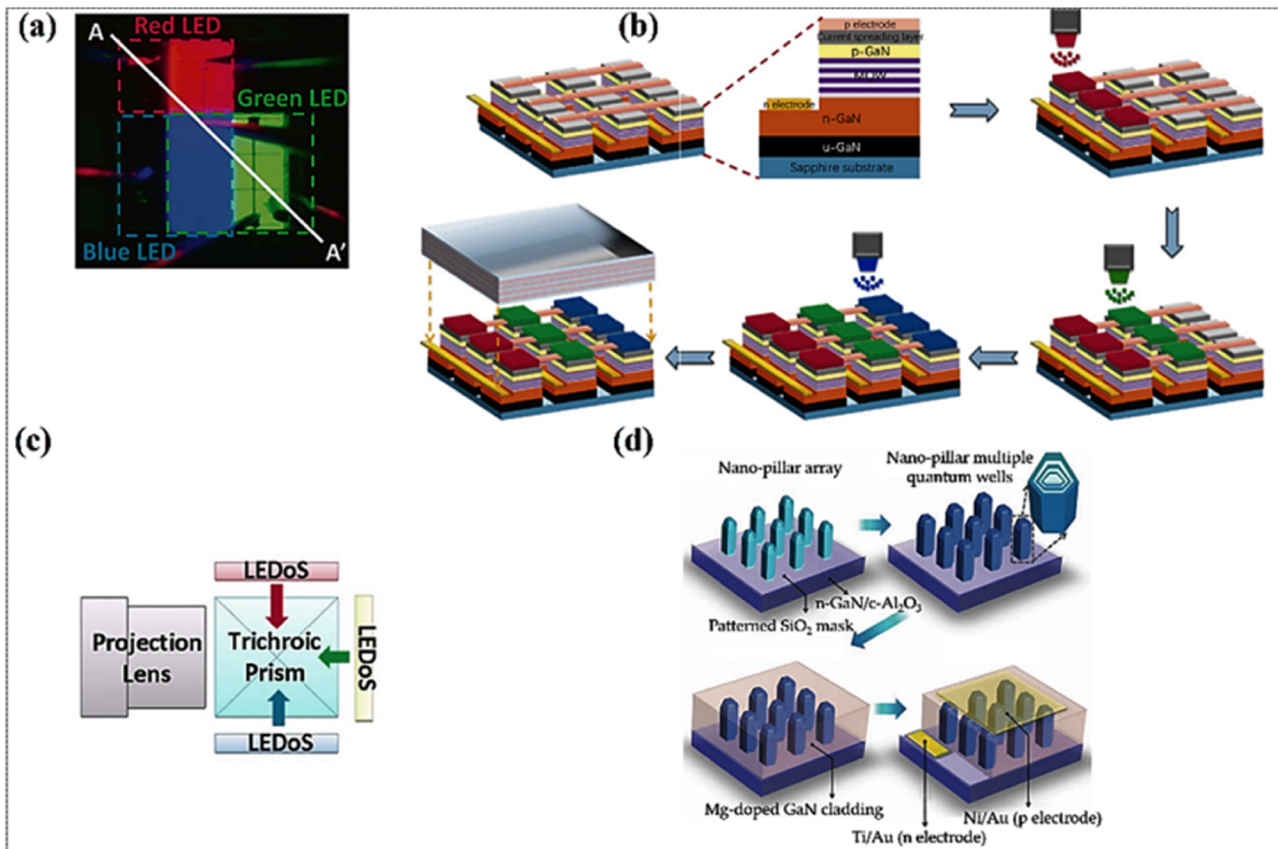


Fig. 14. (a) Image depicting the emission of full-color sub-pixels under a microscope [199], (b) Diagram illustrating the aerosol jet printing technology process [199, 200], (c) Diagram depicting the optical setup of a projection system [199,201], (d) Picture detailing the creation and electron microscope view of embedded nano-pillar LED [199,202].

advancements were made possible by Gallium Nitride (GaN), which consists of gallium (Ga) and nitrogen (N) atoms in a crystalline structure [209]. This has translated into remarkable advancements, such as smaller, more flexible, transparent, and energy-efficient micro-LED displays [186]. Significant progress has been made in optimising the performance and functionality of micro-LEDs. This entails integrating III-V LEDs with silicon thin-film transistors, developing multicolor nanocolumn micro-LED pixels, and creating monolithic integrated devices that contain GaN micro-LED with graphene transparent electrodes [210,211]. These III-V LEDs use semiconductors such as Gallium Nitride (GaN), Indium Gallium Nitride (InGaN), and Aluminum Gallium Arsenide (AlGaAs) to produce a wide range of colors for lighting, displays, and optical communications [212,213]. These LEDs exhibit high efficiency across different technology sectors. Thus, the developmental path has been traced from the evolution of gallium nitride (GaN)-based light-emitting diodes (LEDs) to advanced applications, quantum dot integration, and the cooperation with nanomaterials, indicating perpetual development and potentially beneficial opportunities for the future of micro-LED technology [114,214].

5.1. Nanostructures -enhanced micro-LED display technology

Energy-efficient lighting systems have been significantly transformed by high-brightness LEDs and laser diodes, leading to the development of rugged displays and numerous other technological applications. Notably, there have been remarkable advances in GaN-based LEDs and laser diodes, which underline high luminous efficacy and quantum efficiencies [215]. GaN's role in achieving superior performance characteristics in optoelectronic devices is encapsulated by this progress. Kang's [216] investigation into fabrication techniques as

well as device architectures for GaN nanostructures intended for LED applications has revealed new and practical applications of GaN, which go beyond the possibilities of traditional thinking [216]. This research provides valuable insights to improve the efficiency of GaN-based LEDs by using nanostructure designs. Zhou et al.'s [217] contribution also focuses on new uses for III-nitrides, such as LEDs, solar cells, power electronics, and magnetic devices [217]. It broadens the understanding of III-nitride materials and their versatility in different technological terrains. The focus of Kente on the synthesis, characterisation, and application of GaN nanostructures underscores their importance, about which they represent a comprehensive view in various scientific and technological areas [218]. This research helps to enhance the foundational knowledge for the optimisation of GaN nanostructures. Kente et al. [218] highlights the fact that GaN has revolutionised wireless communications, radar systems, and power electronics by playing key roles in high-power and high-frequency devices. Shur et al. [219] also stresses that GaN surpasses other materials in terms of its superior characteristics, like electron mobility and sheet carrier concentration, which make it suitable for high-power applications [218]. Khan et al.'s [220] discussion of the potential of GaN based devices for power electronics presents the useful aspects of GaN related to better power electronic systems [220]. As pointed out by Khan et al. [220], growth techniques especially MOCVD and Molecular Beam Epitaxy MBE are fundamental in fabrication progression. These methods allow scientists to create nanostructures that are best suited for certain purposes. GaN power devices will have significant performance benefits over silicon. GaN holds the potential to outperform other current technologies in power-related applications. Ambacher's discussion on growth techniques and applications of Group III-nitrides brings out improvements made through advanced growth methods such as metal-organic

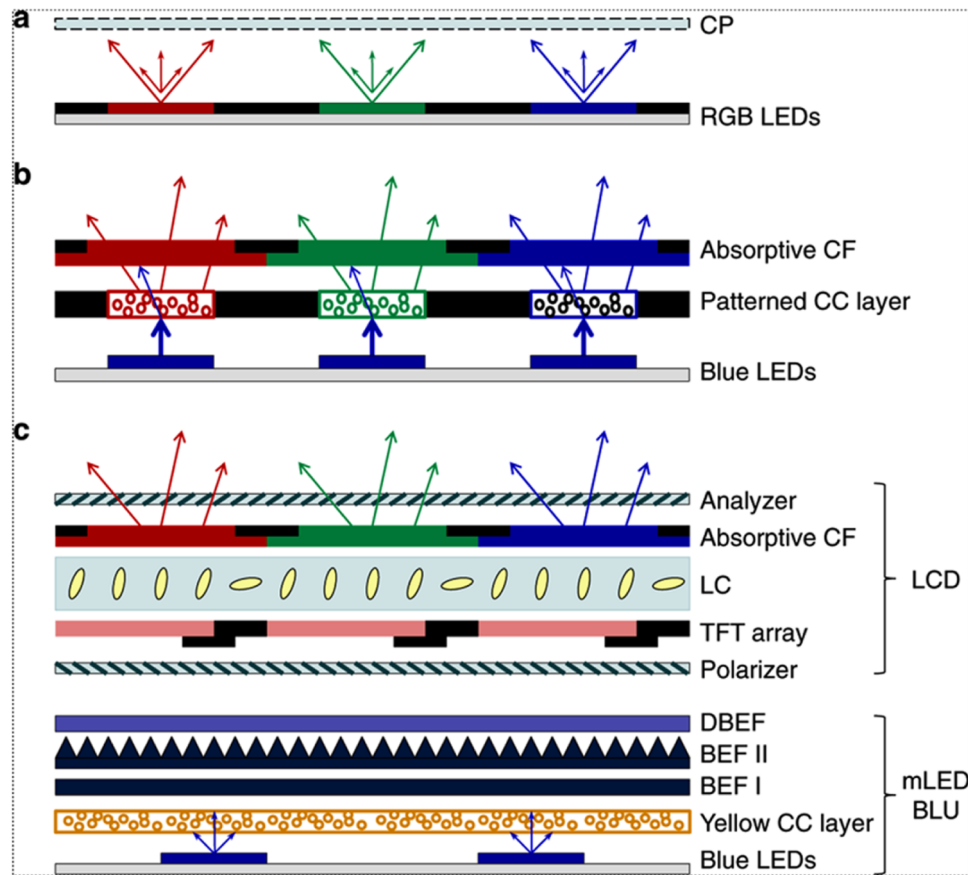


Fig. 15. Three types of displays: (a) RGB-chip emissive displays, (b) Color conversion (CC) emissive displays, (c) mLED-backlit LCDs. In emissive displays (a, b), micro-LED (mLED or μ LED) chips act as subpixels. In nonemissive LCD (c), the mLED backlight is structured into zones, each housing multiple mLED chips to manage panel luminance, each zone can be selectively turned on and off [204].

chemical vapor deposition (MOCVD) and molecular beam epitaxy (MBE) [221]. These insights, in turn, contribute to the constant enhancement of growth techniques for III-nitride materials. Nonetheless, Ambacher et al. [221] has highlighted the challenges in this area, such as minimising material defects, dealing with intricate crystal structures, and curtailing manufacturing expenses [221]. The optimisation of growth processes is crucial to fully harness the potential of GaN and III-nitride nanostructures, necessitating a better understanding of growth mechanisms. It is found that GaN-based LEDs and laser diodes help save energy and improve visual quality [222–224]. These devices are efficient and bright enough, making them important in the development of energy-saving lighting systems. GaN micro-LEDs and Nano-LEDs have been miniaturized, making them versatile enough for the integration of flexible and transparent materials [225–227]. It is a crucial stage toward making these technologies more commercially viable. Production scalability and cost-effectiveness have been improved by several advances in fabrication techniques. These are vital developments towards enhancing the commercial viability of GaN-based LEDs as well as laser diodes. Other than displays, compact LEDs have the potential for automotive lighting and healthcare devices, thereby demonstrating their use in sensing technologies [228,229]. The versatility of GaN-based micro-LEDs and Nano-LEDs is further demonstrated by the widening field of their applications. Advancements in mass production and improvements in display resolutions are some of the key findings from the study on GaN-based micro-LEDs [230]. In spite of notable strides, the issue of crystal defects that affect device performance has remained a central problem. This is vital for commercialisation of high-brightness LEDs and laser diodes [231]. Overcoming these challenges may be resolved by integrating with Si substrates or exploring non-conventional substrates [232]. The approaches give

insight into dealing with substrate materials in the best way. The invention of GaN and III-nitride nanostructures has created another level of high-performance LEDs and laser diodes, providing their versatility and potential across a range of technological domains. Contributions by researchers, from basic material synthesis through growth techniques to power electronics and beyond, have enabled the realisation of such devices. The need for continuous improvement in fabrication processes is emphasised by problems associated with growth processes and substrate materials that hinder the full realisation of these materials' potentials. In addition, Table 3 provides information about RGB micro-LED development, materials, and methods.

Enhanced luminous extraction, reduced inner losses, and precise control of emitted light are crucial functions of nanomaterials like quantum dots and nanocrystals. Micro-LED displays have their light-emitting qualities optimised by quantum dots and nanocrystals [283]. This synthesis leads to higher brightness, improved color accuracy, and a wider gamut, resulting in a better viewing experience. Wu et al. [158], Wu et al. [206], and Jung-El Ryu et al. [70] have also emphasised the effectiveness, as well as the prolonged lifetimes associated with Mini-LED and micro-LED technologies [158]. The recent advancements in thermal management strategies for micro-LED fabrication have led to its enhanced reliability and overall performance [284]. Good thermal management helps maintain the stability of Micro LED displays over time. Not only does the integration of nanomaterials optimise display efficiency, but it also allows for flexible and transparent micro-LED capabilities to be developed [285,286]. There are new opportunities in the fields of curved or bendable screens and transparent displays. When integrating III-V compound semiconductors such as GaN or InGaN with Silicon-based Thin-Film Transistors (TFTs), it is important to ensure compatibility between the materials. Differences in crystal structures

Table 3
Materials/Methods for RGB Micro-LED Development.

Materials/Methods	Remarks	Reference
RGB QDs (CdSe/ZnS, CdS)	RGB QDs emit light at 450 nm, 520 nm, and 630 nm wavelengths when excited by 365 nm ultraviolet light.	[233]
Full-color micro-LED array	Resolution: 128×128 , Pixel size: 35 μm , Pixel pitch: 40 μm . Applied in displays and visible light communication.	[233]
Nanowire Heterostructures	Advantages include reduced dislocation density, polarisation field, improved light extraction efficiency, and compatibility with silicon substrates.	[4,234, 235]
Core/Shell GaN Nanowire Array	Core/shell GaN nanowire LED with modulatable emission wavelength by external voltage.	[236]
Dot-in-a-Wire Nanowire with QDs	Nanowire LED containing QDs (dot-in-a-wire) allowing efficient emissions across the visible wavelength range.	[237,238]
Nanowire LED Arrays via SAG Technology	Nanowire LED arrays with diameters as small as 150 nm prepared through selective area growth (SAG) technology.	[239,240]
Nano-Ring (NR) Structure for RGB LEDs	Implementation of nano-ring structure for tunable emission wavelength and monolithic RGB micro-LED array with red QDs.	[241,242]
Photolithography and Photoresist	Anti-crosstalk window mold with window for QD injection and silver-plated barrier wall.	[243]
Microwave-Assisted Heating	QD solution prepared with a microwave-assisted heating method, resulting in enhanced quantum yield.	[244]
Salt-Sealed QD	RGB color conversion film using salt-sealed QDs for improved reliability and service life.	[245]
Photoresist Hybrid QD	Patterning multi-color QD arrays using a photoresist hybrid, providing a cost-effective solution.	[246]
Photoresist Color Conversion	RGB full-color micro-LED array using a photoresist color conversion layer mixed with QD, achieving high QY.	[10]
Black Matrix Photoresist Spacers	Monolithic RGB QD micro-LEDs with black matrix photoresist spacers for improved light efficiency.	[247]
Funnel Tube Array	Funnel tube array structure for micro-LED display color conversion, reducing color crosstalk and improving efficiency.	[248]
Nanoporous (NP) GaN Structure	NP GaN structure for QD color conversion, improving structural stability and light absorption rate.	[249,250]
Light Emitting Diodes Projector	RGB three-color micro-LED array on silicon with a trichroic prism for a full-color projection system.	[251,252]
ZnCdSe/ZnCdMgSe multi-quantum-well color-converting membrane	Transfer of color conversion film to micro-LED, demonstrating hybrid micro-LED emission.	[253]
InGaN Epitaxial Structure	Micro-LED array with a high In content of up to 0.4, enabling programmable dynamic image display.	[254]
Carrier Blocking Layer (ICBL)	ICBL inserted between multiple QWs in different active regions to guide carriers and improve luminous efficiency.	[255]

Table 3 (continued)

Materials/Methods	Remarks	Reference
GaN, Mg-based p-doping, InGaN Epitaxial growth, GaN-on-sapphire substrates	Enabled development of GaN-based LEDs, Blue emission achieved.	[255–257]
InGaN, p-GaN/n-InGaN/n-GaN DH High-quality growth, Active region of first GaN-based blue LED	Output power: 125 μW , EQE: 0.22%.	[258]
Commercial blue LED by Nakamura et al.	p-AlGaIn-based EBL to prevent carrier overflow. Output power: 1.5 mW, EQE: 2.7%, λ : 450 nm.	[259]
InGaIn quantum well-based blue, green, yellow LEDs	Single-quantum-well devices, Basis for first-generation nitride LEDs. EQE values of 9.1%, 6.3% at blue, green emission wavelengths.	[260]
Substrates: GaN-on-sapphire, semi- and non-polar substrates	Advancements in epitaxial growth techniques. Focus on low-dislocation density GaN substrates, Semi- and non-polar substrates for improved performance.	[261–263]
Non-polar m-plane bulk GaN substrate	Development of high-performance blue LED. Output power: 0.24 mW, EQE: 38.9%.	[264]
Patterned sapphire substrates (PSS)	Low-power blue LEDs, Maximum EQE of 85%, WPE of 81.3%. Development of low-power blue LEDs with PSS.	[265]
Quasi-bulk GaN substrates	High-power violet LEDs, Peak EQE of 73%. Development of high-power violet LEDs with quasi-bulk GaN substrates.	[266]
Flip-chip (FC) technique, Pattered sapphire substrate, Volumetric bulk GaN substrates	Techniques to enhance light extraction efficiency. FC technique enhances LEE by substrate removal, PSS improves LEE, Volumetric bulk GaN substrates achieve 90% LEE.	[266,267]
Modulation-doped AlGaIn/GaN heterostructure	Enhancement of effective hole concentration. Improved conductivity and current spreading characteristics.	[268]
AlGaIn/GaN high electron mobility transistors (HEMTs)	Utilisation of modulation doping for nitride devices. Promotion of sheet carrier densities in the order of 10^{13} cm^{-2} in nitrides.	[169]
GaN-based LEDs for solid-state lighting (SSL)	Disruptive technology in lighting, White LEDs, Ce: YAG phosphor. Commercialisation of first white LED in 1996 by Nichia Corporation.	[269,270]
RGB nitride LEDs for white light	Direct combination of red, green, and blue LEDs. Potential for highest luminous efficacies, RGB arrays on a single chip.	[271]
Nitride-based UV LEDs	AlGaIn-based UV LEDs, Tunable bandgap. Applications in air and water purification, sterilisation, bio and environmental sensing.	[272,273]
III-nitride micro-LEDs	Development for wearable, portable, and futuristic displays. Emerging technology for high-resolution display applications.	[188,274]
Nitride LEDs for Visible Light Communication (VLC)	High-frequency LEDs, VLC systems. Sub-100 μm LEDs for ultra-broadband VLC protocols.	[275–279]
Cryogenic Operation of Nitride LEDs	Asymmetric carrier freeze-out, Polarisation-induced doping. Challenges and techniques for optimising nitride LEDs at or near-cryogenic temperatures.	[280–282]

and properties can make seamless integration difficult to achieve. TFTs are an example of special transistors, which are widely used in the electronics industry, for instance, in making flat panel displays such as LCDs (Liquid Crystal Displays) and OLEDs (Organic Light Emitting Diodes) [212,213]. The confluence of wide-bandgap semiconductors, particularly for LEDs and GaN-based lasers, has various roles to play [284]. With these devices being integrated together, there will be versatile wide-bandgap semiconductor integrated circuits that enhance overall performance and functionality across electronic and optoelectronic domains [284] [287]. Detailed summaries of wide-bandgap semiconductors based on materials, devices, and application perspectives can be found in Fig. 16 [286]. GaN nanostructures have been a major breakthrough, as seen by the success of N-polar InGaN/GaN nanowire green and red LEDs with high efficiency [286] [176,177]. Gallium Nitride (GaN) nanostructures have been highly impactful, particularly demonstrated by the success of high-efficiency N-polar Indium Gallium Nitride/Gallium Nitride (InGaN/GaN) nanowire green and red Light-Emitting Diodes (LEDs) [288,289]. The N-polar orientation, characterised by lateral dimensions as small as 0.7 μm , plays a pivotal role in achieving a flat top surface morphology, rendering these structures highly compatible with fabrication technologies. Notably, the development of an in-situ annealing method for nanowires exhibiting strong red emission has resulted in a remarkable order of magnitude increase in nanowire luminescence [288]. Transitioning to the domain of Ultraviolet (UV) micro-LEDs, their anticipated advantages are tempered by persistent efficiency challenges. Factors such as crystal quality, high-quality p-type Aluminum Gallium Nitride (AlGaN) layers, and UV light absorption have been identified as critical limiting elements [205,290]. This recognition underscores the significance of addressing these challenges to fully harness the potential benefits of UV micro-LED technology, encompassing high efficiency, reliability, fast modulation speed, low cost, and low power consumption. In parallel, the

progress in red micro-LEDs, specifically those based on InGaN, has outpaced standard-sized Aluminum Gallium Indium Phosphide (AlGaInP)-based red LEDs in terms of luminous efficiency [161,291,292].

Quantum dots and nanocrystals still play a pivotal role in optimising light extraction, minimising internal losses, and enabling precise control of emitted light. The exploration of quasi-2D Ruddlesden–Popper perovskites (2D-RPPs) is a promising way to achieve deep blue color in LED displays [294]. These perovskites can overcome challenges through fast crystallisation, leading to possibilities for improved vividness and quality displays. Quasi-2D Ruddlesden–Popper perovskites represent an intermediate class between conventional 3D and purely 2D perovskite structures. Perovskites are materials that have gained a lot of interest due to their unique crystal structure and a wide range of applications, especially in optoelectronics such as solar cells and light-emitting devices. In the perovskite family, the Ruddlesden–Popper phase or type refers to a layered structure [295]. Large-scale manufacturing of III-V LEDs with silicon TFTs and perovskite LEDs is challenging. For the advancement of such modern display technologies, it is necessary to develop new fabrication techniques [296]. One-way breakthroughs are made to improve the stability of perovskite LEDs involves using a dipolar molecular stabiliser. This innovation results in highly effective and durable perovskite LEDs that can last a long time, thereby addressing questions about the durability of advanced display technologies. Long-term operation tests, as well as accelerated aging experiments, provide useful information about the functional behavior and reliability of perovskite LEDs under operation. To obviate typical flaws found in the top-down LED design, plasma etching is not required when nano or micro-LEDs are developed through a bottom-up method. This helps avoid surface defects common in conventional top-down LEDs, although they remove the need for plasma etching. Improved charge carrier recombination quantum efficiency of

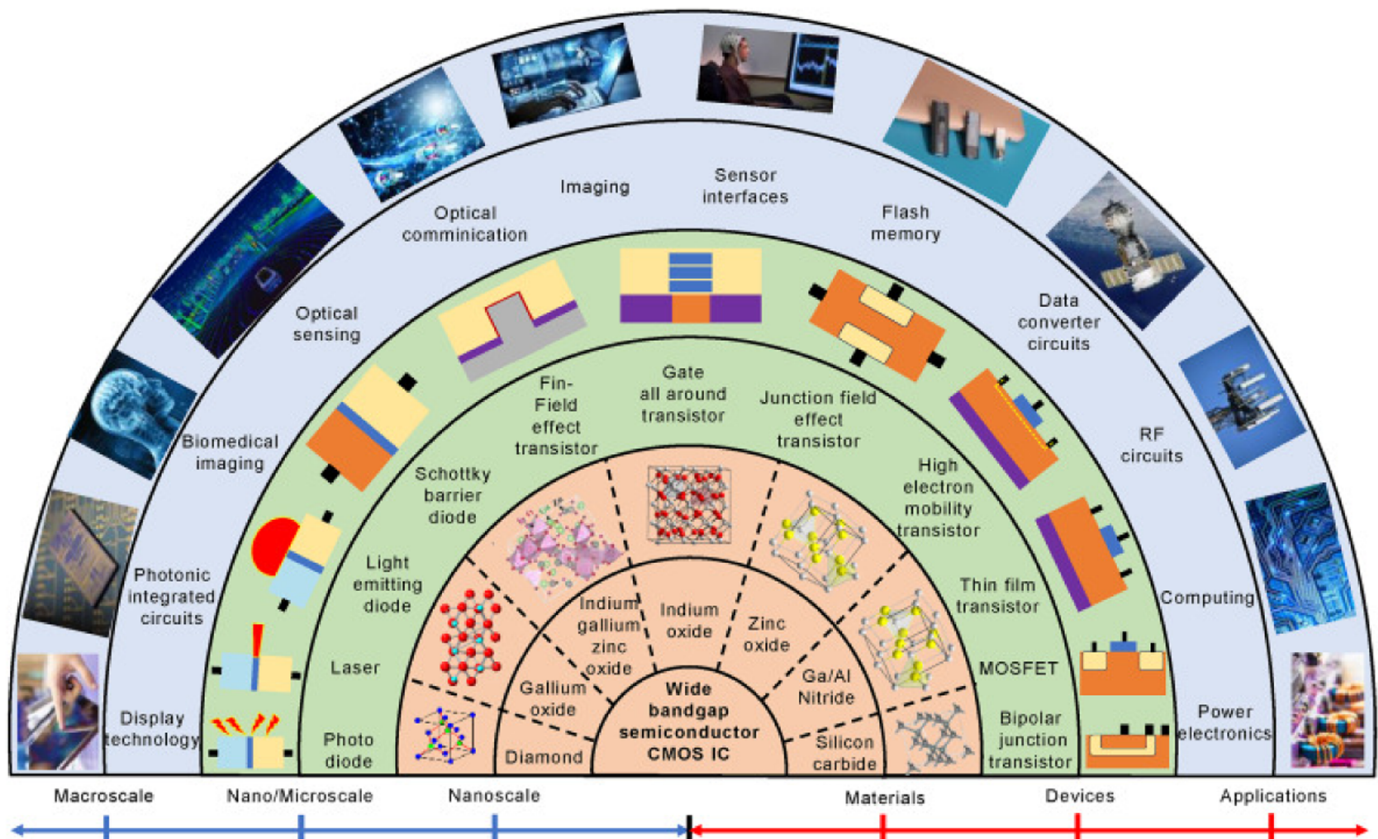


Fig. 16. Detailed summary of wide bandgap semiconductors based on materials, devices, and application perspective[293].

dislocation-free crystal growth on various substrates is facilitated by metal-organic chemical vapor deposition (MOCVD) and molecular beam epitaxy (MBE) [235,297]. Strain relaxation enhances dopant incorporation, which is important for maximising injection efficiency in LEDs [298,299]. A core-shell structure resulting from wide-bandgap AlGaIn incorporation in InGaIn nanostructures shields the active region from surface recombination effects. Precise definition of nanostructure dimensions, shape, morphology, and placement before epitaxy can be achieved using selective area epitaxy (SAE) with both MBE and MOCVD [300,301]. Hence, growing crystals only in exposed substrate areas allows the production of desired tailored nanostructure arrays. Multi-color pixels and micron-scale platelets, pyramids, nanowires were reported by Kishino et al.'s studies [302]. Recent research by Wang et al. [303], [37, 304] however, demonstrated green LED matrices with a peak EQE of ~9% and red-emitting micro-LEDs with a peak EQE of 1.75%, both grown utilising selective area epitaxy [37,304]. For instance, an integration between a micro-LED and a high-electron-mobility-transistor (HEMT) resulted in a modulation bandwidth of 1.2 GHz, which is the highest ever recorded in this context [305]. By arranging nanostructures in certain configurations, photonic crystals or metasurfaces can be created to enhance emissions. It is possible to design the photonic crystal to increase light extraction efficiency, direct emission, and reduce spectral broadening. This strategy has found application in nanostructure-based surface-emitting lasers (PCSELs) [180,306]. The LED technology is depicted in Table 4 through the revolution from GaN nanostructures to Ultraviolet (UV) micro-LEDs and the rapid advancement of InGaIn-based red micro-LEDs. Such technologies are exemplified by key nano-materials, their properties, and fabrication details of nanostructures for micro-LED technologies, as indicated in Table 4. High efficiency, spectral line width, among others, are some of the main properties that highlight their importance in micro-LED technologies. The activities encompass MOCVD growth, ALD, and PECVD processes, which are useful for on-wafer packaging advances and addressing issues arising with regard to material quality.

5.2. Perovskite mini-micro-LED stability and efficiency

A significant milestone has been recorded in the field of display technology by the Perovskite LED (Light-Emitting Diode) technology, thereby demonstrating momentous advancement. The unique crystal structure (ABX₃) used to build these LEDs has some benefits like high color purity, adjustable bandgap, and ease of making [323]. Of all the materials available, metal-halide perovskites that possess optoelectronic features akin to those of III-V materials have demonstrated potential for application in light-emitting diodes (LEDs) [324,325]. Among the organic halide perovskite quantum dots (OPQDs), scientists are actively tackling the challenge of lattice instability and surface defects in metal halide perovskites (MHPs) [326]. There are new methods being explored such as F anion doping to increase stability [327]. Furthermore, it is used as a protective shield against lattice decomposition when applying SiO₂ coating. Specifically, CsPbBr₃:KBF₄@SiO₂ nanocrystals (NCs) exhibit good stability along with 85% photoluminescence quantum yield (PLQY) [328] [329]. Perovskite LED breakthroughs and structural insights are found in Fig. 17, indicating the relationship between structural characteristics and stability/performance improvements. As depicted in the upper left panel of Fig. 17, the perovskite LED structure comprises detailed intricate elements. The right-hand panel of the Figure presents a graph representing T50 lifetimes of LEDs with respect to their optical power output (radiance) [296].

Numerous long-term stability tests and accelerated aging were performed to glean insights on the durability of perovskite LEDs. Stability data is summarised in Fig. 18, which encompasses the findings from all these experiments. The left panel presents results of long-term operation and accelerated aging experiments, while the right panel offers external quantum efficiency data, aiding in comprehending efficiency

Table 4

Overview of nanostructures for micro-LED technologies.

Nano Materials	Properties	Remarks	References
N-polar InGaIn/GaN nanowire arrays	High efficiency (EQE up to 25%), lateral dimensions as small as 0.7 μm	N-polar orientation yields flat top surface, compatible with fabrication. Reversed polarisation reduces electron overflow. In-situ annealing enhances red emission in nanowires.	[172,288, 289]
InGaIn photonic crystal nanowire	Spectral linewidth of 4 nm, one order of magnitude smaller than conventional quantum well LEDs	Enables individually addressable pixel illumination, bringing maskless photolithography possibilities.	[183,307]
Novel micro-LED structure design with quantum dots (QD)	Anti-crosstalk solution	Optimal optical design using Monte Carlo method, addressing crosstalk problem, and introducing chip micro-LED + QD fluorescence mode	[308]
GaN-based micro-LED arrays with tailored fluorine ion implantation	Improved uniformity, reduced leakage currents	Pixelation strategy using F ion implantation, multi-energy injections for uniformity, and reduced reverse leakage currents	[309]
InGaIn/GaN multiple quantum wells micro-LEDs with piezo-phototronic effect	Strain visualisation, color response	Dual-wavelength micro-LEDs demonstrating strain visualisation, significant color response under mechanical stimulation	[310]
GaN-based micro-LEDs with ion implantation	Improved luminous efficiency, reduction of surface defects	Ion implantation method to modify sidewall surface defects, enhanced photoelectric performance, increased external quantum efficiency (EQE)	[311]
SiO ₂ -coated CdSe/ZnS quantum dots	Photoluminescence enhancement, fluorescence decay reduction	Synthesis of SiO ₂ -coated CdSe/ZnS QDs with tunable emission wavelengths, improved PLI and decay rate, integration into photoresist for micro-LED display, design of a DBR layer for full-color micro-LED display	[312]
Nickel oxide nanoparticles (NiO NPs)	Adjustable optoelectronic properties, high photosensitivity and detectivity for UV photodetector	Synthesis using co-precipitation, precursor concentration effects studied, high photosensitivity and detectivity, high photosensitivity (1100%) and specific detectivity (4.324 × 10 ⁻¹⁰ Jones) for	[313]

(continued on next page)

Table 4 (continued)

Nano Materials	Properties	Remarks	References
Quantum dots (QDs) in metal/oxide/semiconductor junction (LE-MOSJ)	Ultrahigh-resolution display (4200 PPI), simple structure of ITO/Al ₂ O ₃ /QDs/Ag	diode evaluated at 120 mW/cm ² Proposed QD-based light-emitting MOS junction (LE-MOSJ) with sub-10 micrometer device array, achieves ~4200 PPI LE-MOSJ array	[314]
Aligned s-CNT array (A-CNT)	High mobility, large driving current	A-CNT TFTs for mLED/ μ LED backplanes with exceptional performance, surpassing commercially available TFTs	[315]
SrCeO ₃ :Eu ³⁺ nanopowders	Orthorhombic structure, red emission	Versatile red-emitting SrCeO ₃ :Eu ³⁺ nanopowders for displays and forensic applications	[316]
Nanocrystals in glasses	High-throughput, thermally stable, micrometer-scale luminescent patterns	Laser-induced inverted patterning for micro-LED color conversion layers	[317]
Poly(methyl methacrylate) (PMMA) resist patterns in micro-nanoscale	Uniform, reproducible micro-nanoscale patterns	AFM mechanical ploughing for cost-effective fabrication of OLED arrays with pixels of 500 nm and 1 μ m	[318]
GaN-on-Si micro resonantbased blue micro light emitting diodes	Improved characteristics, reduced sidewall damage	Ion implantation and resonant cavity structure in nanorange for enhanced stability and optical characteristics in GaN-based micro-LEDs	[319]
GaN-related quantum wells	Correlation between photoluminescence and electroluminescence in GaN-related micro-LEDs	Systematic analysis considering carrier escape, applied bias, leakage current, incident light absorption, and carrier accumulation	[320]
SiO ₂ -coated perovskite quantum dots	Luminous properties preservation, color conversion	Synthesis of cadmium-free perovskite QDs with SiO ₂ passivation, application in blue micro-LED display, improved color purity using distributed Bragg reflector (DBR)	[321]
Perovskite nanocrystals (PNCs)	PNC-polymer composites with superhydrophobic surface for stable and efficient white LEDs	Highly luminescent PNC-polymer composites with superhydrophobic surface, enhanced chemical stability, improved out-coupling efficiency	[322]

improvements [296] [18]. The use of light emitting diodes based on LTA zeolite with quantum dots (QDs) of CsPbBr₃ has been demonstrated to be stable and possess high photoluminescence quantum yield (PLQY) [329,330]. This study demonstrates that incorporating QDs into zeolites may lead to high stability over a wide range of optical properties in perovskite-based systems. In this regard, a novel method has been

introduced for fabricating continuous perovskite films using high-resolution micro-patterning technique. Ultra-high-resolution displays (UHDs) can be achieved cost effectively by employing PeLEDs [331]. One-pixel PeLEDs which have effective area dimensions of 500 $\times$ 200 μ m² to 100 $\times$ 200 μ m², and exhibit EQE values as high as 9.1% are a reality now. A Lithium fluoride thin film interlayer in the nanometer scale is the key for such success because it relieves surface tension and makes possible robust integration of high-efficiency PeLED architectures via self-aligned photolithography [332].

Notwithstanding remarkable advancements in research on PeLEDs, the attainment of efficient PeLED micro-sized pixels remains an unsolved issue. Recent investigations have found EQEs that are above 20% and IQEs close to 100% [333,334]. There is limitation in options for improved emission efficiency from micropatterned emissive arrays in PeLEDs hence this becomes a barrier to further improvements [335, 336]. Advances in perovskite mini-micro-LEDs have demonstrated great promise in terms of stability and efficiency. This field has seen major developments due to path-breaking strategies, materials, and fabrication techniques. At the same time, challenges and ongoing work indicate that perovskite-based LED technologies continue to seek better performance.

6. Commercialisation challenges and applications of micro-LED development

Obstacles to the commercial viability of small-area micro-LEDs include challenges in efficiency, particularly for InGaN variants using green and red. Emission characteristics, quantum efficiency, and color stability are negatively affected by irregular indium distribution. Current research is directed towards advancing material engineering to achieve precise indium control, a key factor for highly efficient colors. Reduced quantum efficiency due to crystal defects like dislocations in III-nitride nanostructures underscores the need for defect mitigation to improve crystal quality. Improved growth techniques are required to reduce defects while enhancing the structural integrity of III-nitride nanostructures [304,337]. In spite of these hurdles, III-nitride nanostructures, especially gallium nitride (GaN), hold great promise across various applications [338]. Marketable products are made from the discoveries in nanotechnology, but they face many obstacles, such as high initial production costs [338]. To make it in the competitive domain of advanced materials, startups require strategic alliances [339]. Nitride development success is dependent on managing technical challenges and making wise choices of substrates that can be scaled up and be cost-effective [340]. The possible solution to this challenge is explored by examining bulk GaN growth. This will be done through optimising crystal growth parameters, developing cutting-edge techniques, and gaining more insight into the underlying processes. Ultimately, this will lead to enhancing the efficiency and scalability of gallium nitride (GaN) production, which will then result in better device performances and cost-effectiveness [341]. The need for the standardisation of protocols in quality control for III-nitride microelectronic devices can never be overstated. To ensure uniformity, reliability, and consistency in manufacturing, these aspects are highly critical. Precise measuring capabilities and reproducibility are provided by them, while benchmarking through addressing issues of variability further enhances the overall quality of devices [342]. Zhao et al. [343] explores unconventional substrate growth for III-nitride nanowires, introducing innovative design possibilities[343]. Pandey et al. [344] focuses on III-nitride nanostructures for high-efficiency micro-LEDs and optoelectronics, bringing advantages such as enhanced efficiency, precise wavelength control, durability, compact size, wide bandgap for efficient light emission, versatility in design, energy efficiency, compatibility with semiconductor processes, and contribution to technological advancements[344]. In this case, Dehghani et al. [345], Galbraith et al. [346], and Arora et al. [347] have all underlined the importance of commercialisation in nanotechnology as a potential source of economic growth requiring efficient technology transfer and adaptation to market

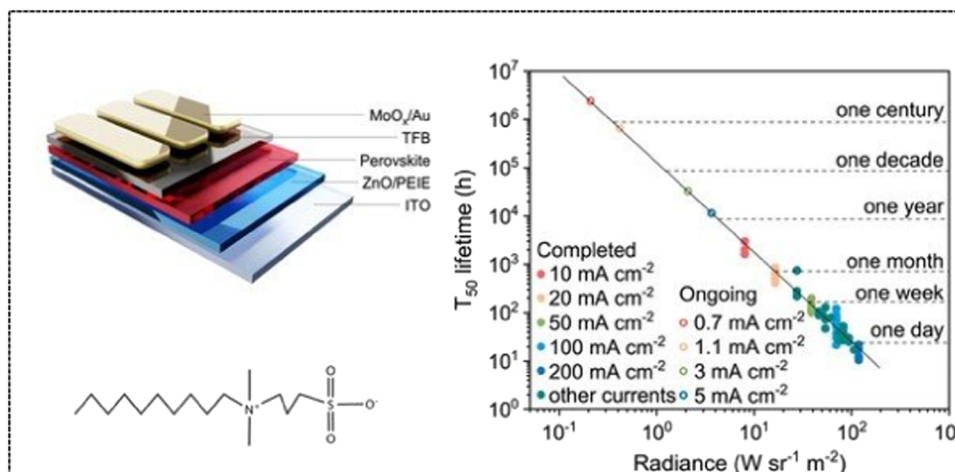


Fig. 17. Structural insights into the perovskite LED are presented in the upper left panel, the right panel of the figure displays a graph illustrating the T50 lifetimes of the perovskite LEDs in relation to their optical power output (radiance)[296].

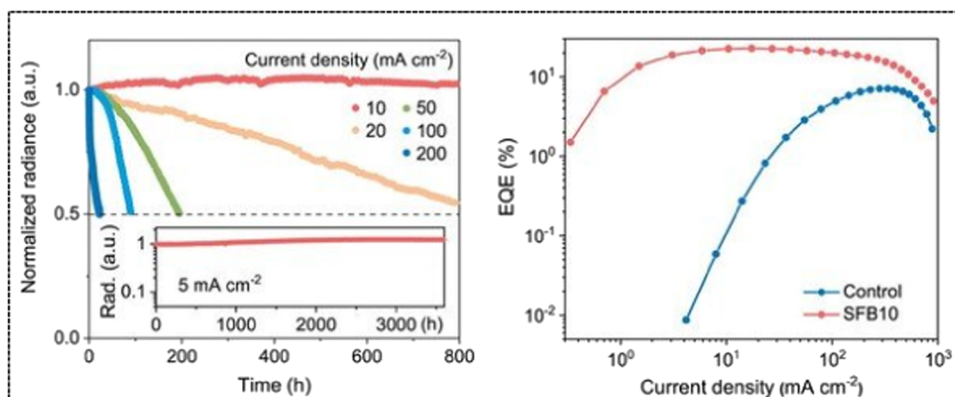


Fig. 18. The left panel of this figure illustrates the results of long-term operation and accelerated aging experiments conducted on the perovskite LEDs. The right panel of the figure displays external quantum efficiency data, comparing the stabilised devices with untreated ones[18].

demand. Teece et al. [348] argues that there is a need for continued investment in Research and Development for refining micro-LED technology (MLED), [348] which has the potential to provide better display quality, be energy saving and versatile [349]. The efficiency challenges associated with large-scale manufacturing using pick-and-place techniques are recognised; thus, ongoing investigations are focusing on developing highly scalable robotics systems and automated processes [350]. This addresses issues such as: hurdles in efficient assembly, full-color schemes, defect management and cost control [351]. Mass transfer problems and manufacturing costs should be resolved [157]. Micro-LEDs have a huge potential to change our visual experience. Chen et al. [352] emphasises that mass transfer techniques for high-density micro-LED arrays are important[352]. Furthermore, the work of Bower et al. [353] focuses on high-brightness displays using micro-transfer printed flip-chip micro-LEDs[353]. Conversely, Zhou et al. [354] addresses growth, transfer printing, and colour conversion techniques for full-color micro-LED displays[354], including the challenges involved in achieving uniform color distribution, managing defects, and optimising cost-effectiveness [355]. On the other hand, integration challenges for micro-LEDs include defect management, improving yield rates, and balancing efficiency and scalability [356] [357]. However, Ahmed et al. emphasises the importance of epitaxy costs, transfer and defect management [351]. Also useful are Lee et al.'s [356] views on the potential of micro-LED technologies for high-performance emissive displays[356], as well as Cok et al.'s [358] explanation about the benefits of micro-transfer printing in creating

inorganic micro-LED displays[358]. The integration of manufacturing techniques and automation can be helpful. High-quality displays cannot do without quality control protocols and defect management systems. Currently, the study is aimed at optimising the production processes, enhancing mass transfer effectiveness, and improving integration approaches. It is important for micro-LED manufacturing to emphasise automation optimisation to achieve cost-effectiveness and efficiency [359]. Tape assisted Laser Forward Transfer (TALFT) process for high-accuracy wafer-scale micro-LED transfer has been considered by research [151], while fluid self-assembly suggested by Ji et al. [360] as a different approach for transfer of micro-LEDs[360]. These benefits show that micro-LED possesses many positive traits such as longevity, high efficiency among others [361]. The challenge now is to make nanoLEDs work with pixel sizes of all sizes [362]. Li et al.'s [363] research shows that this technology could develop Mini-LED products with low power consumption and high reliability [151,363]. LED chip fabrication, as well as placement uniformity and cost-efficiency, is also hampered by Ryu et al. [70]. On the other hand, quantum dots and phosphors are confronted with various challenges, including low conversion efficiency as well as potential toxicity [214,364]. While there are obvious benefits to using quantum-dot-based color conversion, it leads to more layers in the manufacturing stage. However, despite this long-term inefficiency, III-nitride inorganic devices, particularly micro-LEDs, continue to be impressive [46,365]. Among several other approaches, like tunnel junction devices and excitonic LEDs that are based on nanostructures [146,366]. For example, in the presence of diverse applications beyond

display screens, such as those based on nanostructures for micro-LEDs, may hold significant potential. Optical interconnects that use micro-LED technology promise low-power and high bandwidths to overcome limitations posed by electrical links over long distances [367, 368]. Micro-LEDs display a combination of features for use as biological sensing devices that are capable of harvesting energy wirelessly and transmitting signals [369]. In imaging dyes, fluorescence spectroscopy, and neuronal stimulation, nitride-based micro-LEDs are applied [370]. The flexible nanostructure-based approach opens the way for developing distinctive micro-LEDs for specialised applications. Nanostructures are central to overcoming the hurdles faced in nanoscale and micron-scale optoelectronic devices. This makes the selective-area epitaxy method more effective towards the bottom-up formation of photonic crystals [371,372]. Strain relaxation efforts and the suppression of parasitic recombination in SPSSL layers are targeted at red-emitting micro-LEDs. To overcome efficiency droop in red-emitting micro-LEDs, the AlGaIn electron blocking layer is examined. Tunnel junction contacts may also be used in N-polar devices grown via selective area epitaxy [373–375]. The droop effect of nanowire micro-LEDs at higher injection currents remains problematic. Narrow-linewidth, spectrally pure, high-efficiency micro-LEDs can be achieved on a single chip through integration into photonic crystals in optimised devices. The sensitivity of nanostructures to dimensions emphasises the need for precise substrate control prior to epitaxy and meticulous fabrication post-epitaxy. Ionicity in polar semiconductors, such as InGaIn, affects electronic, optical, and excitonic properties. Studies have shown that the exciton binding energy increases dramatically in nanoscale III-nitride heterostructures compared to bulk ones [376,377]. Polaronic excitons, with a binding energy as large as 190 meV in GaIn nanowires, illustrate the interaction of excitons with phonons, affecting charge carrier transport, relaxation, and recombination [378]. However, challenging it is, micro-LED technology has several advantages, including display quality and energy efficiency, that, coupled with its versatile applications and ongoing advancements, make it promising for the future of display technology.

7. Concluding remarks

The paper reviews advanced display techniques and explores the opportunities prevalent in this area. In the realm of micro-LED displays, the incorporation of nanomaterials, particularly focusing on nanocrystals and quantum dots, represents a significant leap in display efficiency, color accuracy, and resolution. This advancement holds potential benefits for industries such as electronics, healthcare, and automotive. The integration of III-nitride nanostructures into micro-LEDs contributes to high brightness levels and low energy consumption rates. However, successful commercialisation faces challenges related to scalability, cost-effectiveness, and standardisation. Another approach to consolidating multiple functions into one chip, simplifying system configuration and reducing space requirements, is through Monolithic Integrated Device (MID) technology. Pervasive use of MID technology can be seen in areas like consumer electronics, healthcare, and industrial control systems. The latest developments concern top-down and bottom-up approaches to micro-LED integration, allowing the merging of III-V LEDs with Silicon Thin Film Transistors (TFTs), well-known for leading towards the most recent improvements in high-performance multifunctional devices with high efficiency. However, some attention must be paid to steps that are necessary for understanding manufacturing complexity, uniformity, and cost-effectiveness in order to have better products. Manufacturing complexity, uniformity, and cost-efficiencies are required before these technologies can fully deliver on their promises. This may eventually bring about a world where markets rely on displays that are not just of high quality but also flexible and power-saving.

Declaration of Competing Interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper

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